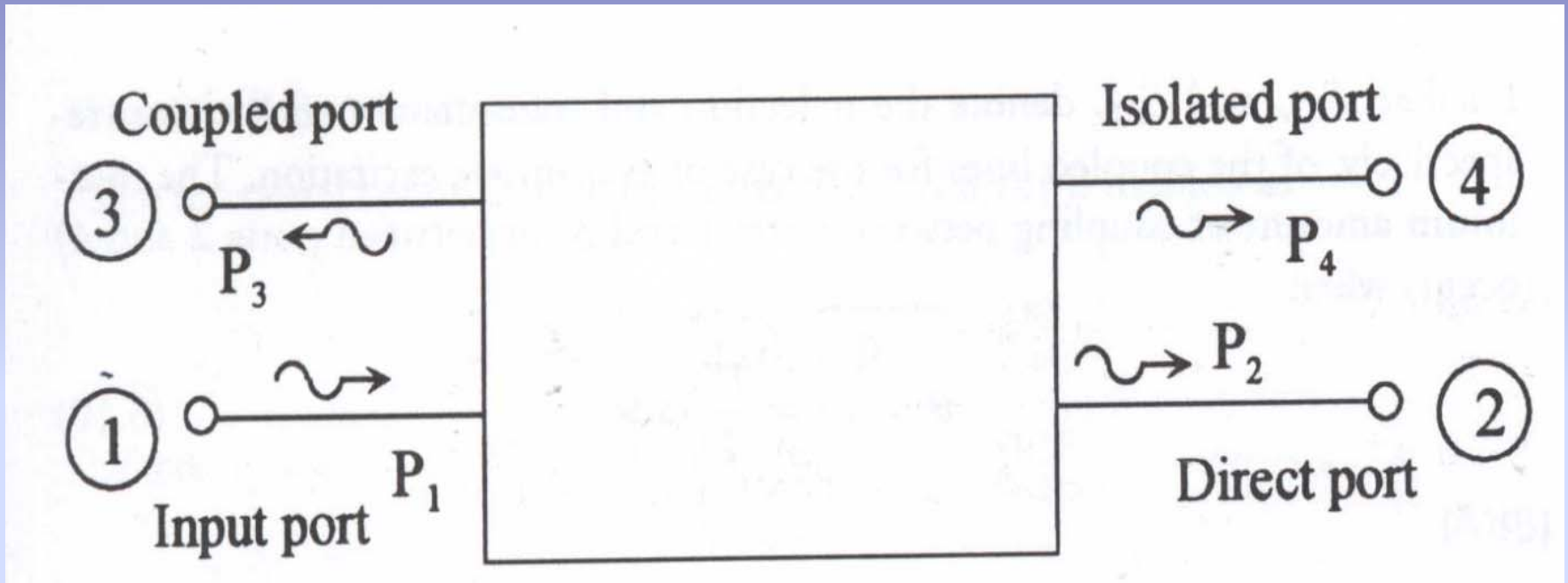
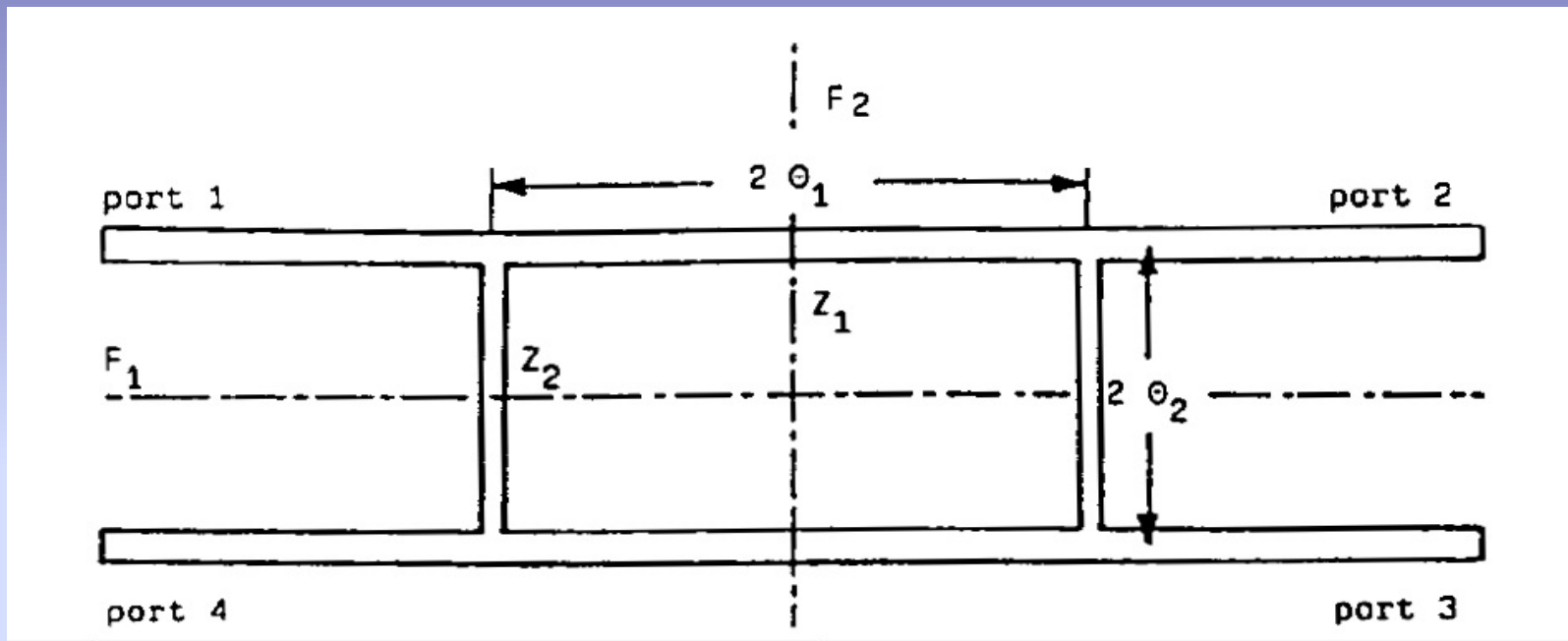




Directional couplers are used so often in microwave systems and networks. They are passive circuit elements. Some functions of directional couplers can be defined as power sampling of sources, analyzing of incident and reflected signals, dividing and combining power among a number of loads.

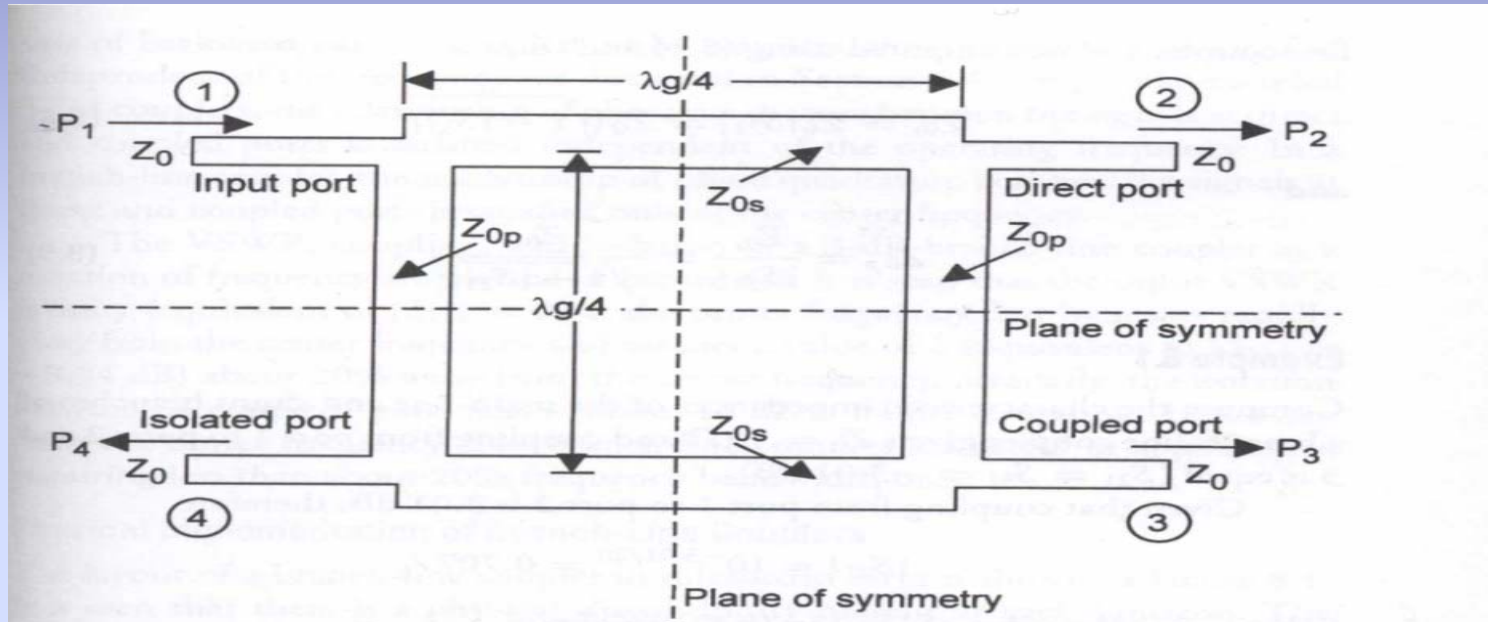


- Coupling (dB) =  $10 \log \frac{P_1}{P_3}$
- Isolation (dB) =  $10 \log \frac{P_1}{P_4}$
- Directivity (dB) =  $10 \log \frac{P_3}{P_4}$

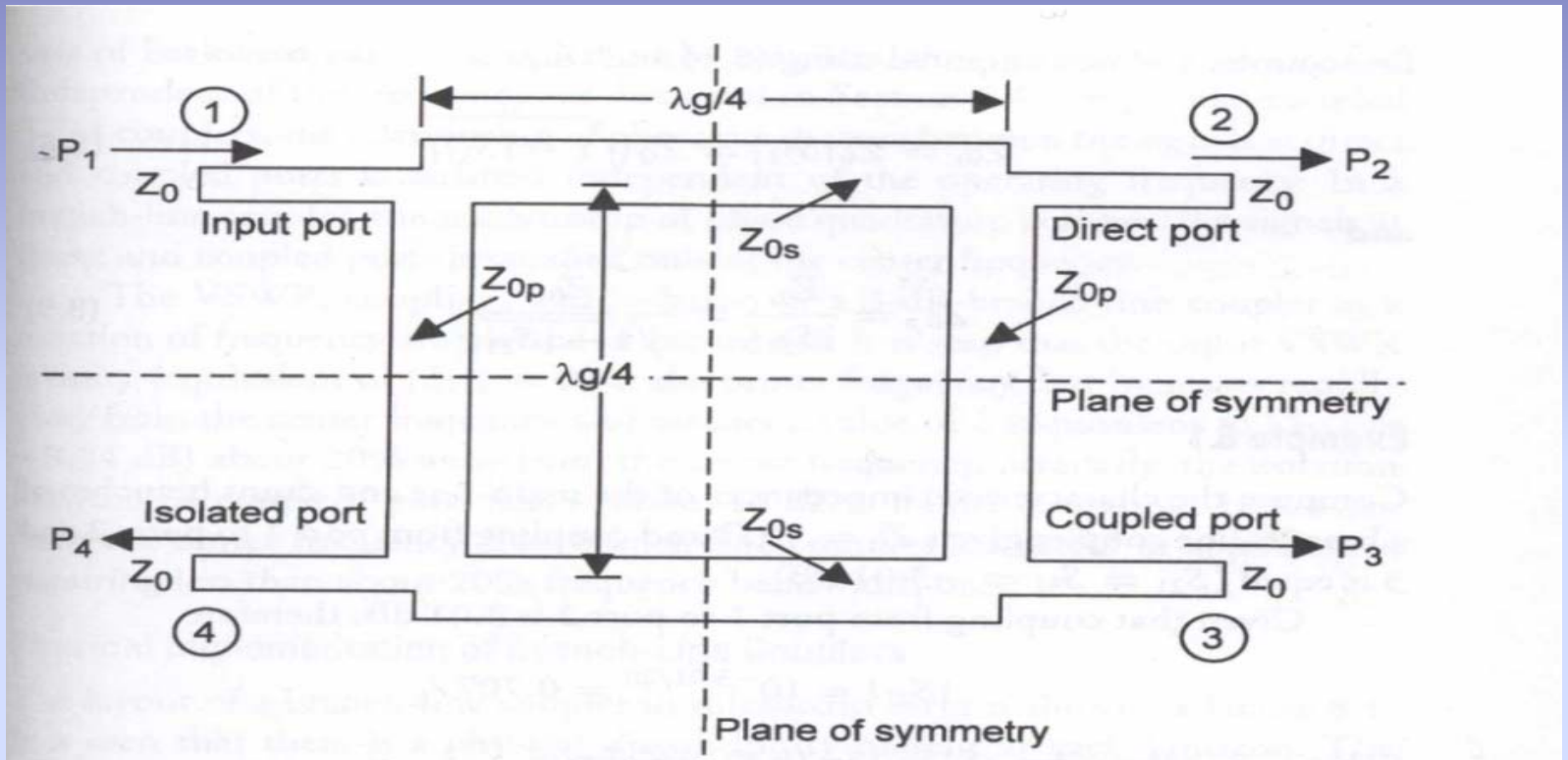


- Isolation (dB) = Coupling (dB) + Directivity (dB)
- In ideal case directivity should be infinite. It means that power output equals zero at port 4 (isolated port). But in reality, there is a small amount of coupled power at port 4.

# Branch Line Coupler



Branch line coupler is a kind of directional coupler which consists of two transmission lines and two shunt connected branches between these transmission lines. They have the properties of directional couplers. Transmission lines and branches are generally quarter wave long.



$S_{11}$  = reflection coefficient at port  $i, i=1-4$ ;

$S_{21}$  = transmission coefficient

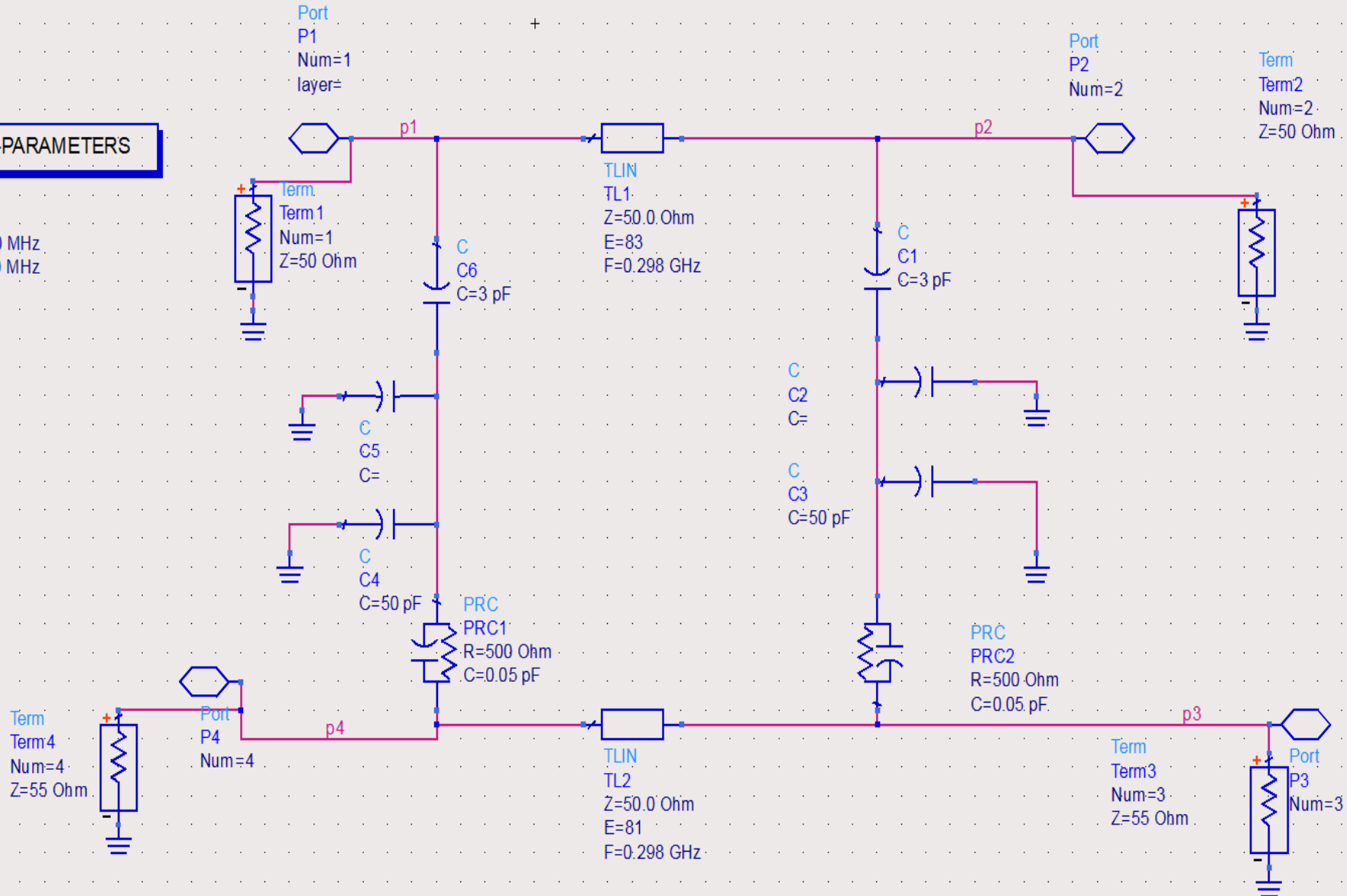
$S_{31}$  = coupling coefficient

$S_{41}$  = Isolation

# Advanced Design System

## S-PARAMETERS

S\_Param  
SP1  
Start=290 MHz  
Stop=310 MHz  
Step=



# 8085 smd capacitors in laboratory



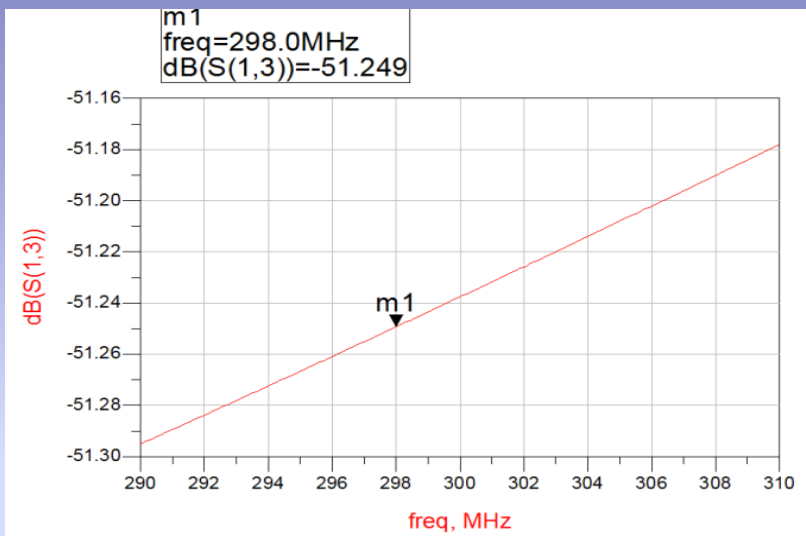


Figure -  $S_{13}$  for  $C_2$  and  $C_5$  equal 50 pF

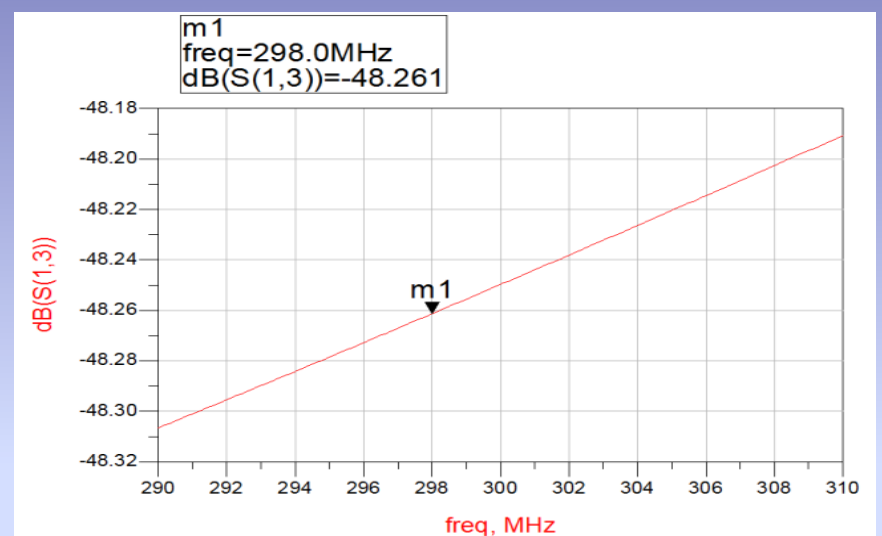


Figure-  $S_{13}$  for  $C_2$  and  $C_5$  equal 20 pF

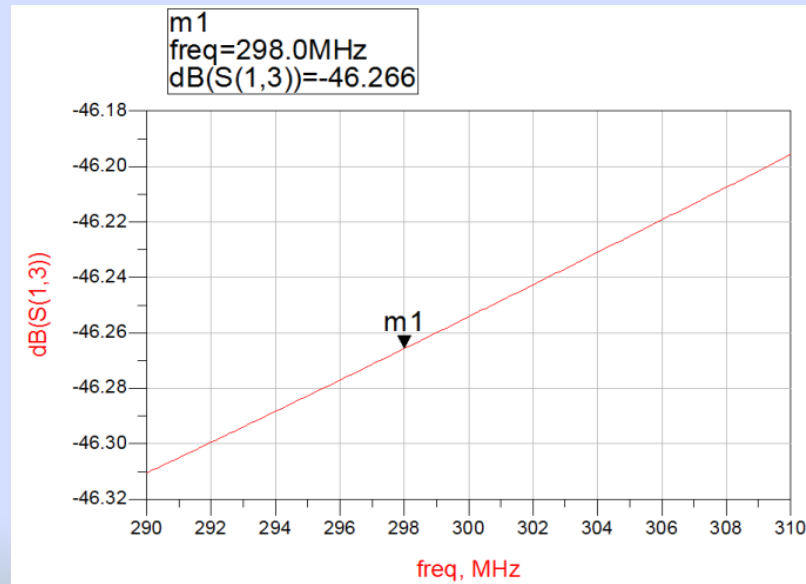


Figure-  $S_{13}$  for  $C_2$  and  $C_5$  equal 5 pF

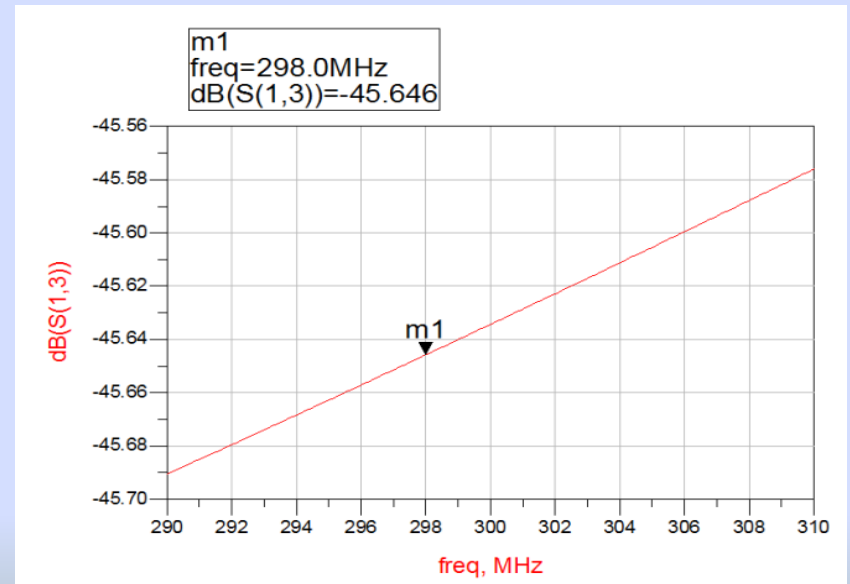
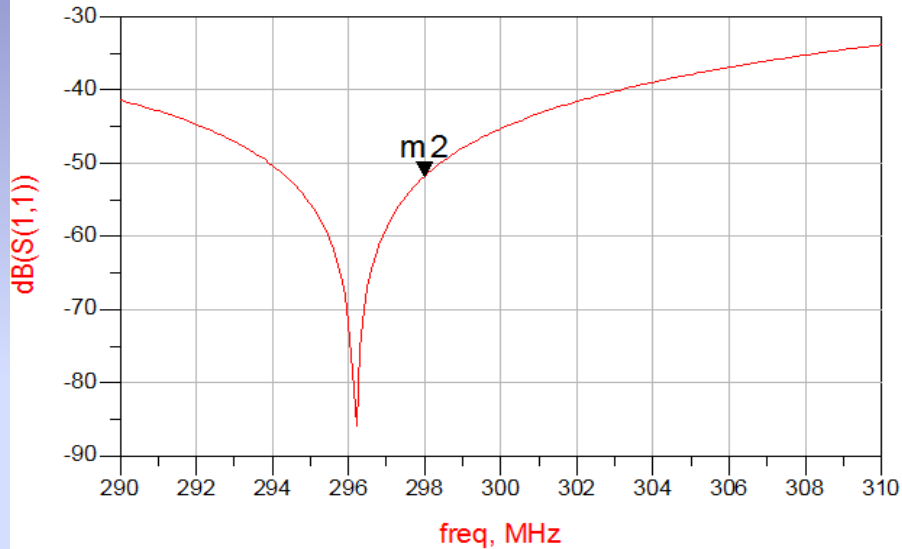
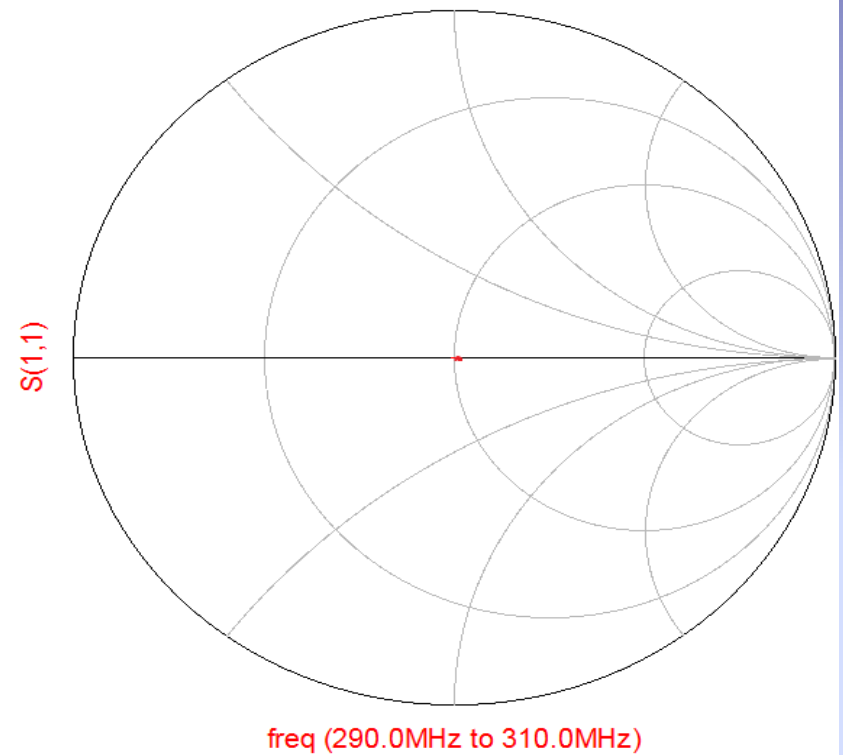


Figure-  $S_{13}$  for  $C_2$  and  $C_5$  equals 1 pF





m2  
 freq=298.0MHz  
 dB(S(1,1))=-51.771



Reflection coefficient is a ratio of forward travelling wave voltage and reflected wave voltage.  $S_{11}$  is reflection coefficient. It should be zero for a perfect 50 ohm match.

Ratio of reflected wave to incident wave is very small “-51.771 dB”. We can consider that the input signal travels through the line and not reflected at the end

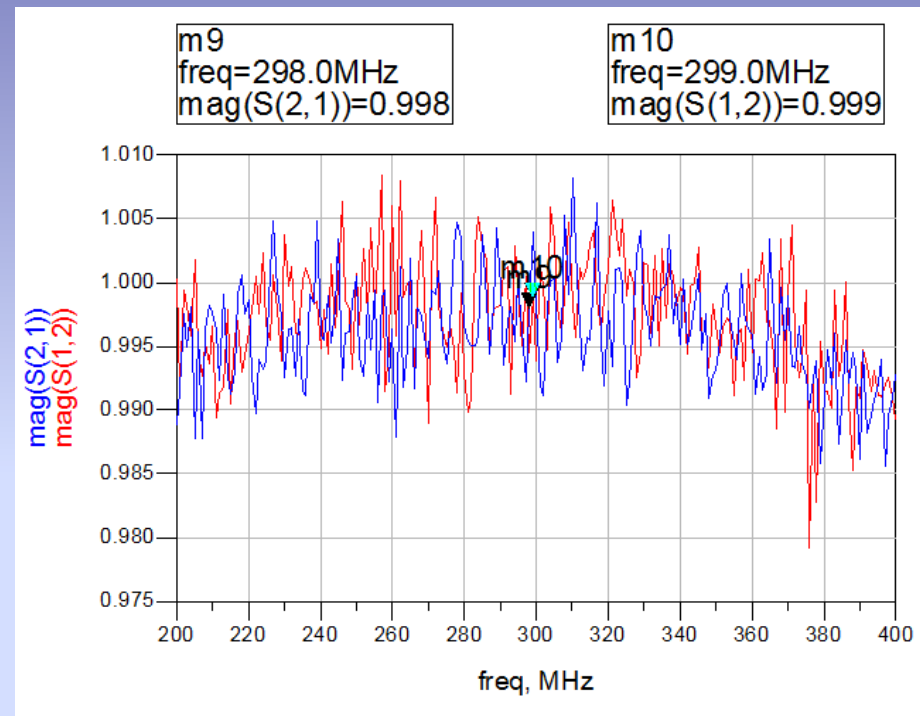
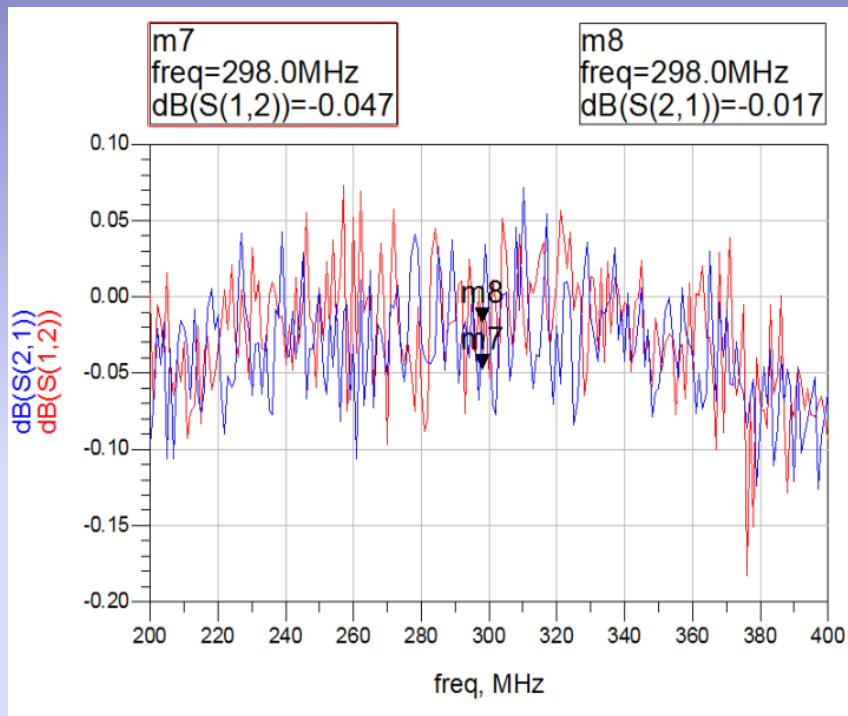


Figure -  $S_{21}$  transmission coefficient (dB and magnitude)

- As shown in figure, ratio of output voltage to input voltage is nearly "0 dB". The magnitude is "1". It means that nearly all of the travelling signal passes through and none of that input signal is reflected.

m4  
freq=298.0MHz  
dB(S(4,1))=-89.264

m5  
freq=298.0MHz  
mag(S(4,1))=3.442E-5

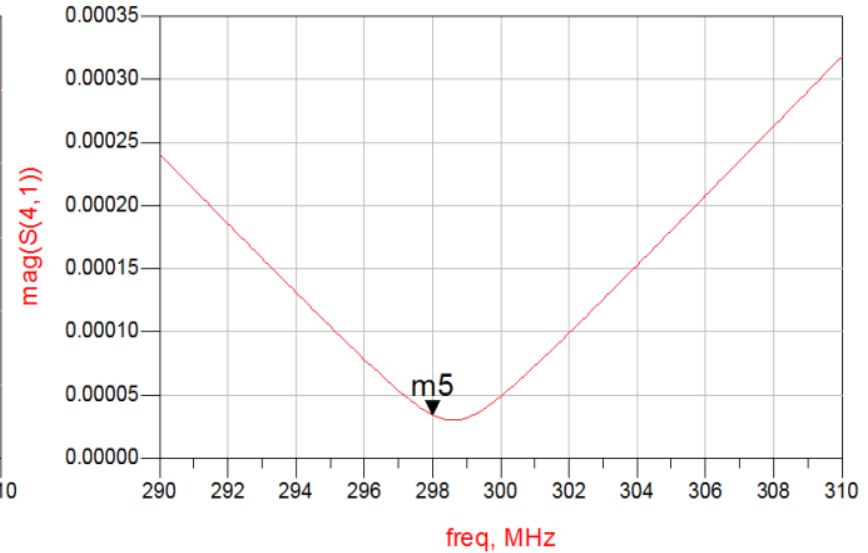
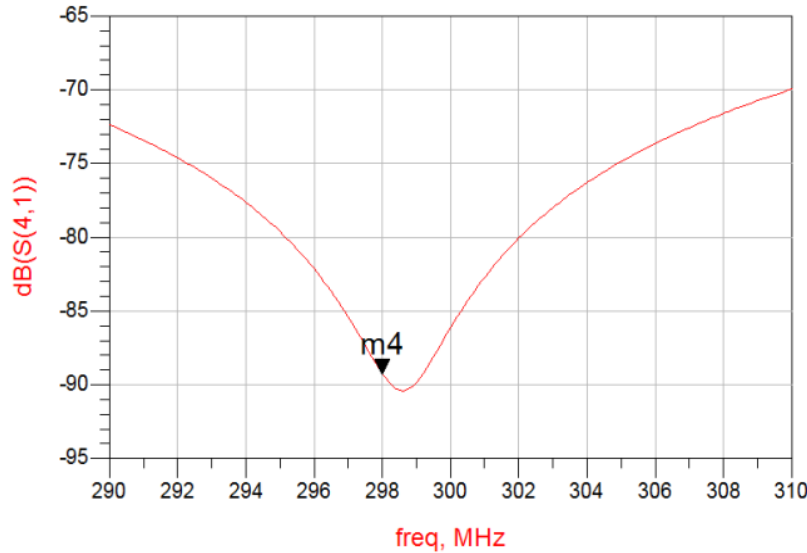
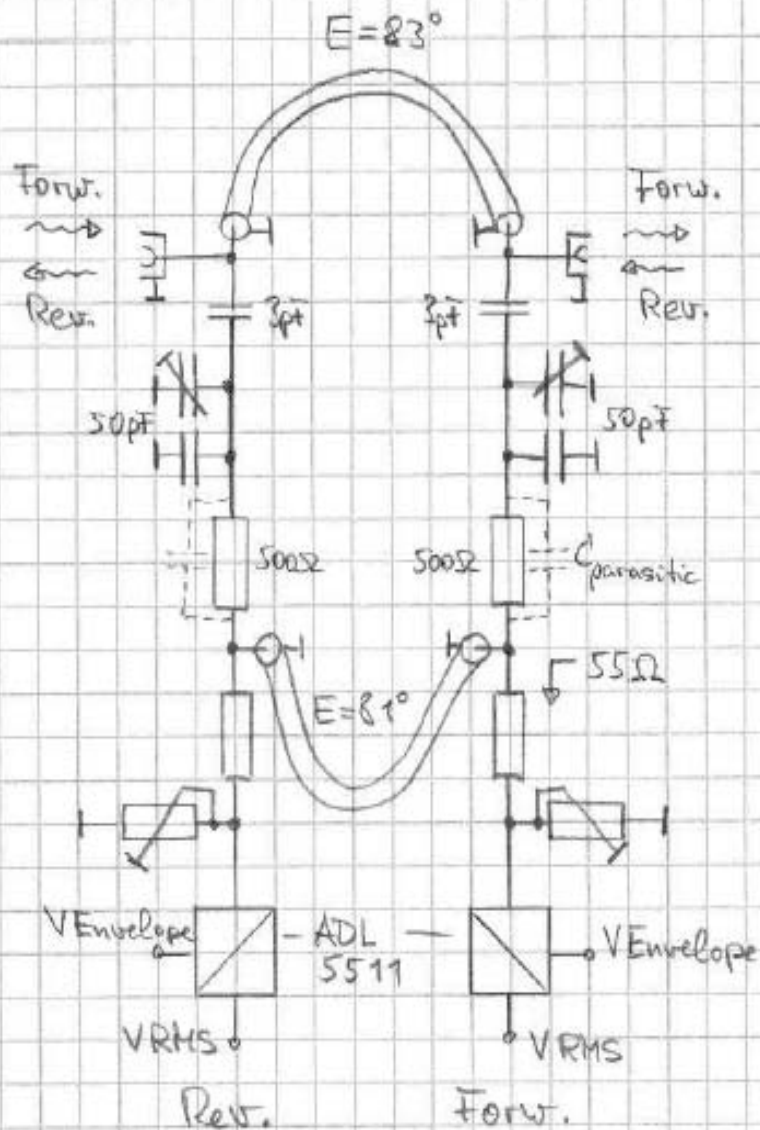


Figure21-  $S_{41}$  isolation

- $S_{41}$  is isolation .If all input signal travels through the line, it means that port 4 is isolated from that signal.  $S_{41}$  equals “-89.264 dB” and less than  $S_{31}$ . “-45.646 dB”. It proves that our design works as a directional coupler.
- - 89.264 dB = - 45.646 dB + Directivity(dB)
- Directivity = ” -43.618 dB”



In our directional coupler design between second transmission line and ADL5511, there is another circuit which is designed for tuning to adjust attenuation. The power amplifier employs 1 kW (60 dB) high power level, it can effect components ADL5511 which are working with low power. Attenuations should be calculated for safety power margin. 55 ohm match is required for the circuit under directional coupler. We assume that ADL5511 has a resistance of 150 ohm. Circuit includes one trim resistance parallel to ADL5511 and another series trim resistor to that parallel connection.

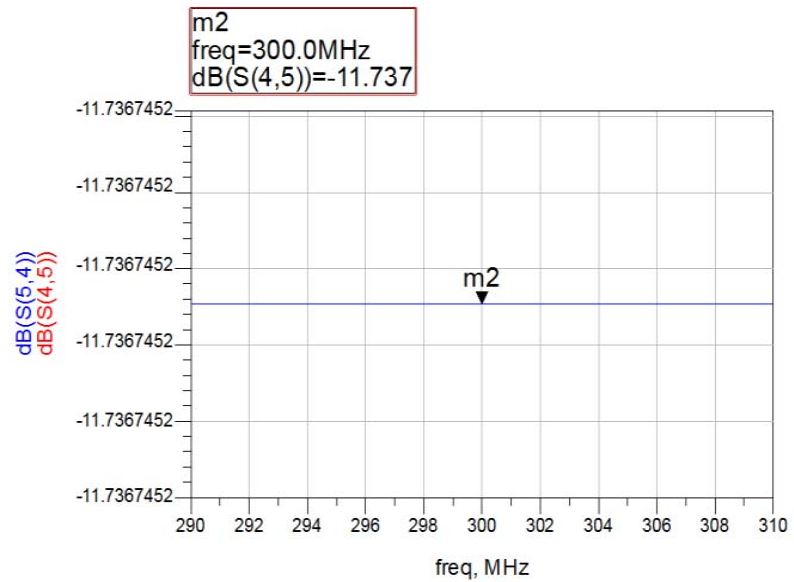
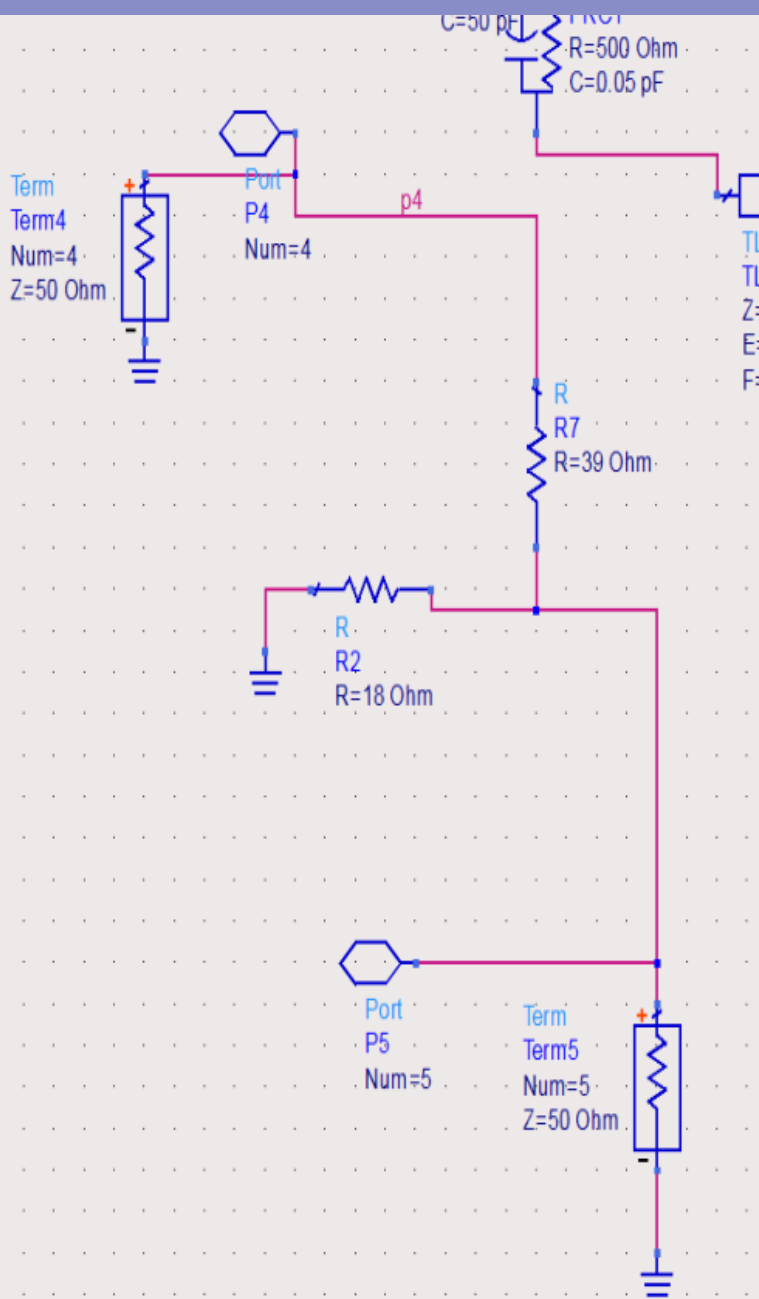
For 55 ohm match:

$$1/(1/(150) + 1/R_a) + R_b = 55$$

approximately;

$$R_a = 18 \text{ ohm}$$

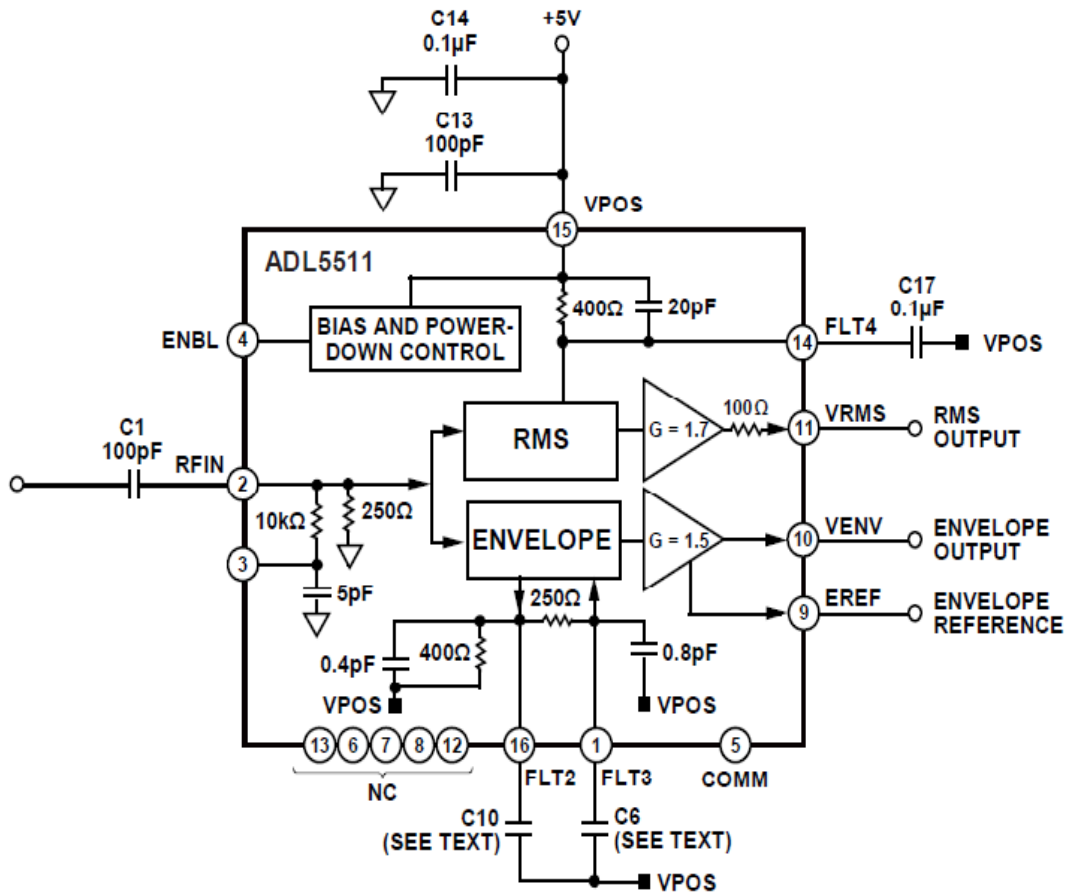
$$R_b = 39 \text{ ohm}$$



ADL5511 has a maximum input level 17 dBm and minimum input level -29 dBm.

1 kW (60 dBm) input power,  
 “-45 dB” coupling factor,  
 “-11.737 dB” attenuation

$(60\text{dBm} - 45\text{dB}) - 11.737\text{dB} = 3.263 \text{ dB}$



Basic connection description in datasheet ADL5511 includes 75 ohm resistance at RF input pin2 for 50 ohm matching. But our design has two trim resistances between transmission line and ADL5511 to adjust attenuation. This 75 ohm resistance must be removed. For operations below 1GHz, FLT3, FLT2 and FLT1 should be decided by adding external capacitors. As mentioned in datasheet our device has a low pass filter for RF input signal. This low filter consists of 10 kΩ and a capacitor 5 pF. This corresponds to a corner frequency of approximately 3.2 MHz. If the carrier frequency is less than approximately ten times this value (32 MHz), this corner frequency must be reduced. Our operating frequency is 300 MHz. So that filter1 is not needed to be used.

- When FLT2 and FLT3 are not connected, for envelope output ADL5511 has two internal low pass filters with 1 GHz and 800 MHz to remove carrier signal of RF. To reduce the corner frequency 200 MHz, external capacitors for these two filters are 1.6 pF and 2.4 pF.

- $C_{FLT2} = \frac{1}{(2\pi \times f_{FLT2} \times 400\Omega)} - 0.4 \text{ pF}$

- $C_{FLT3} = \frac{1}{(2\pi \times f_{FLT3} \times 250\Omega)} - 0.8 \text{ pF}$

Filter 4 is used to set the corner frequency for rms averaging circuit.

$$C_{FLT4} = \frac{1}{(2\pi \times f_{FLT4} \times 400\Omega)} - 20 \text{ pF}$$

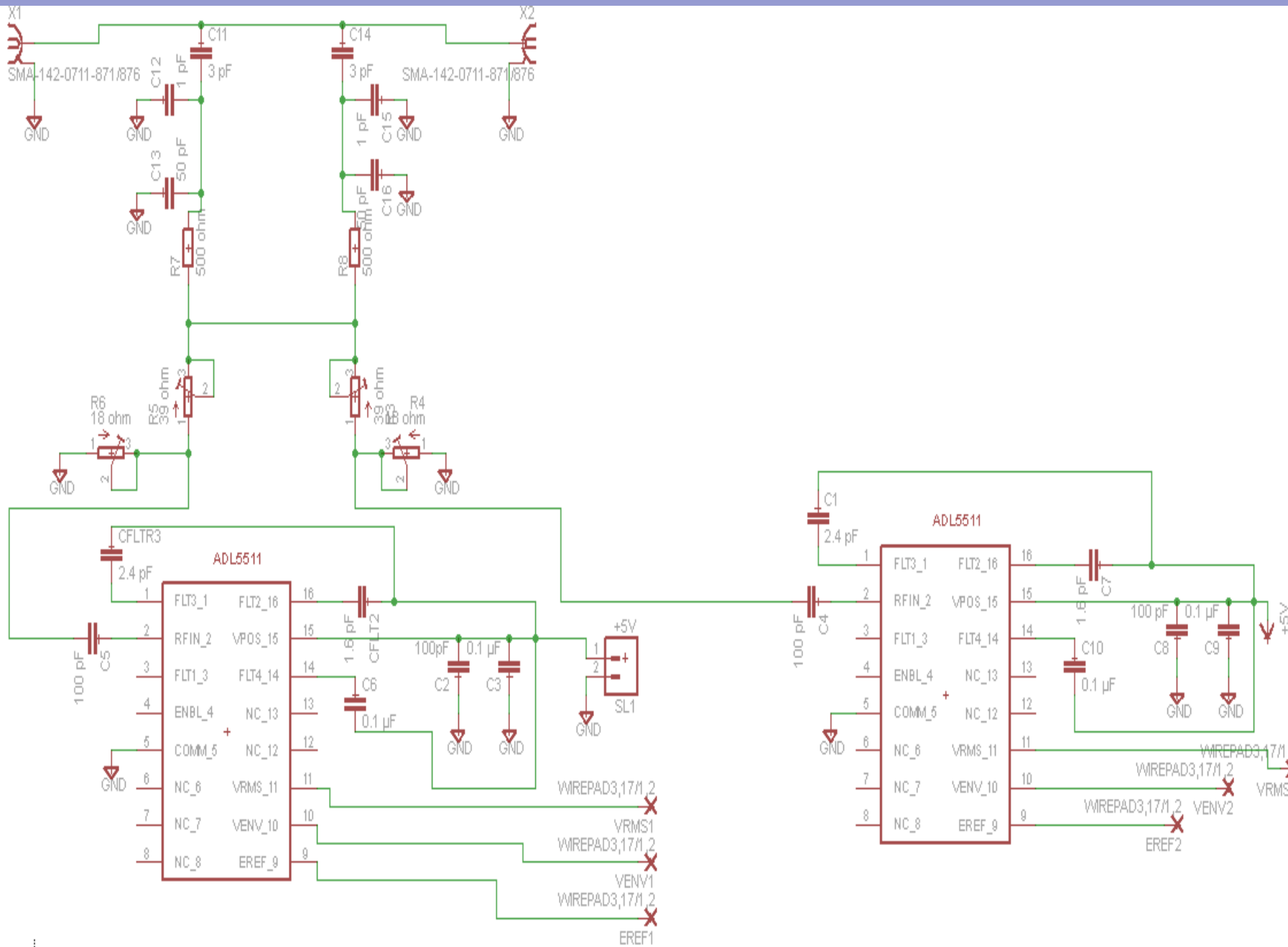
100nF is needed for filter4 to reduce the corner frequency of rms output to 4 kHz

- When FLT2 and FLT3 are not connected, for envelope output ADL5511 has two internal low pass filters with 1 GHz and 800 MHz to remove carrier signal of RF. To reduce the corner frequency 200 MHz, external capacitors for these two filters are 1.6 pF and 2.4 pF.

- $C_{FLT2} = \frac{1}{(2\pi \times f_{FLT2} \times 400\Omega)} - 0.4 \text{ pF}$

- $C_{FLT3} = \frac{1}{(2\pi \times f_{FLT3} \times 250\Omega)} - 0.8 \text{ pF}$

# schematic design





Transmission lines were simulated first with microstrip lines

$$C_0 = 299,792,458 \equiv 3 \cdot 10^8 \text{ meters per second.}$$

$$f = 300 \text{ MHz}$$

$$\epsilon_r \text{ dielectric constant of RO4003} = 3.38 \pm 0.05.$$

$$\lambda = V_{ph} \cdot T = \frac{V_{ph}}{f} = \frac{2\pi}{\beta} = \frac{C_0}{f \sqrt{\epsilon_r \mu_r}}$$

The length of the transmission line can be calculated now approximately.

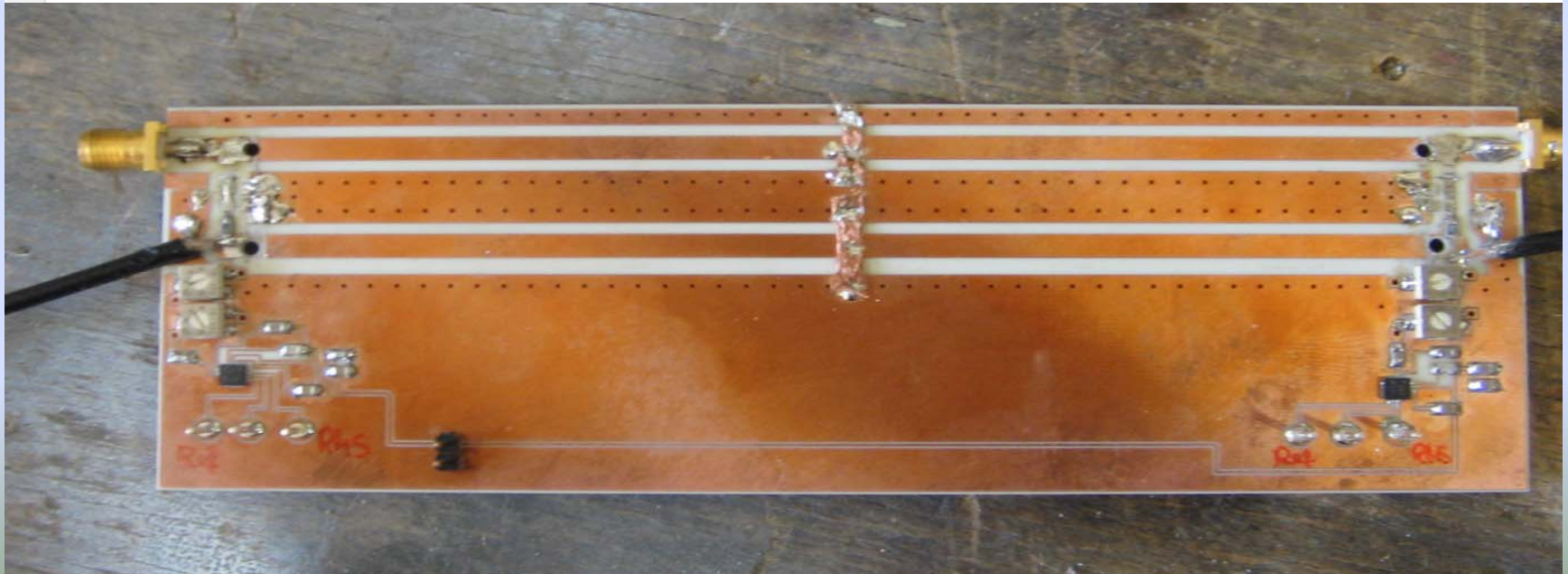
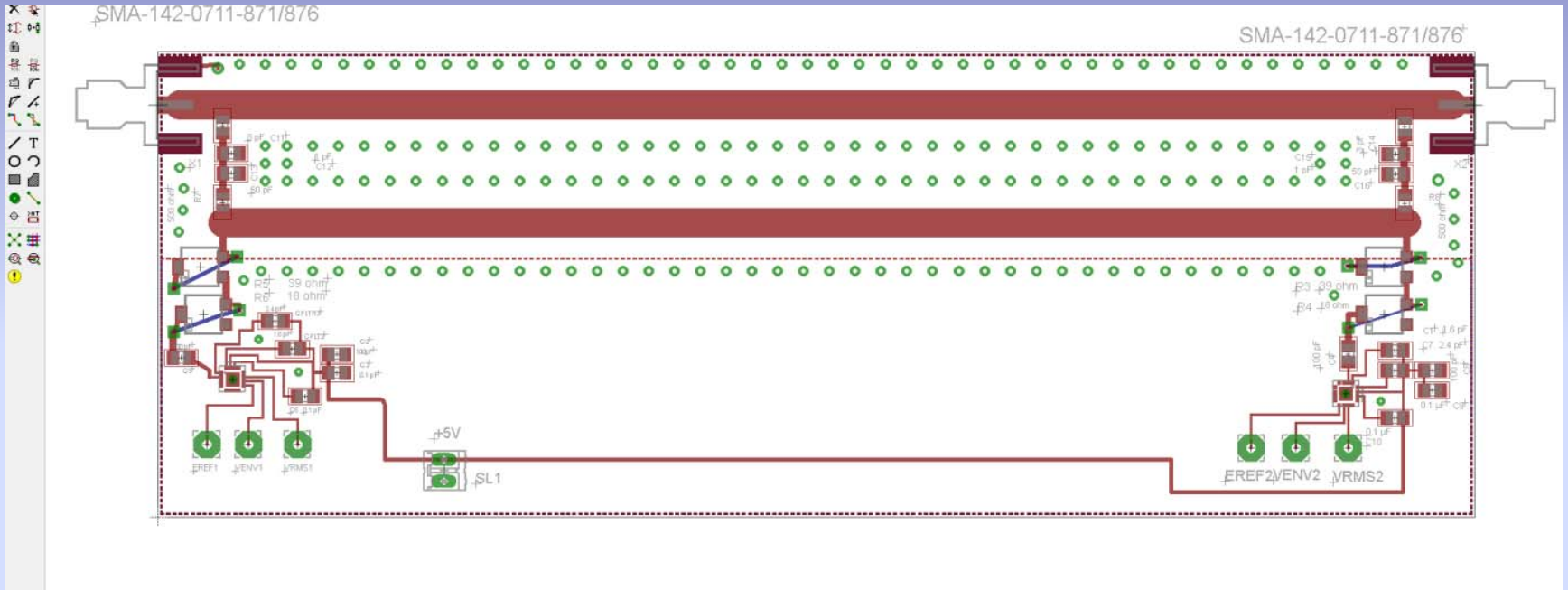
$$\frac{3 \cdot 10^8 \text{ m/s}}{300 \text{ MHz} \cdot \sqrt{3.38}} = 0.5439 \text{ meter for } \lambda$$

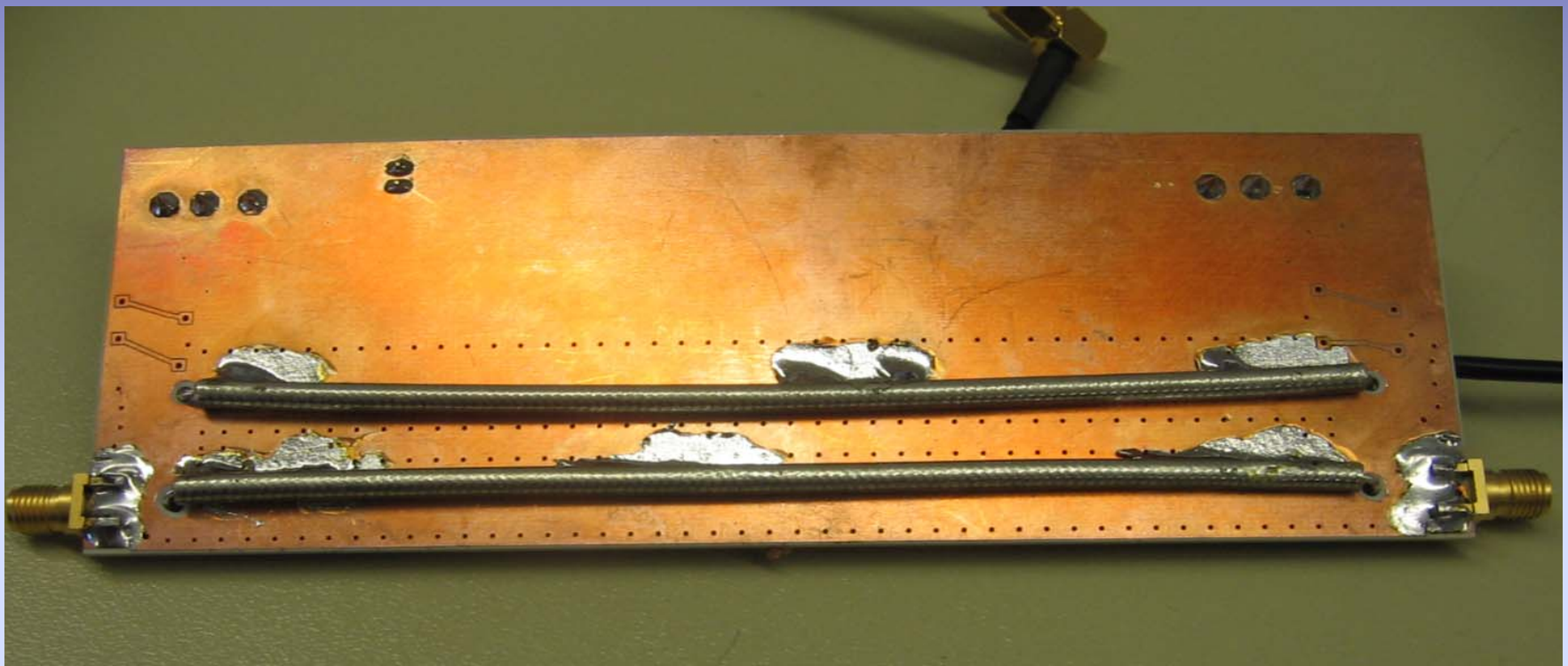
$$\text{for quarter wave length } \frac{\lambda}{4},$$

$$0.5439 / 4 = 0.13598 \text{ meter} \equiv 136 \text{ mm}$$

With the help of a computer program using more parameters due to error functions, the length of transmission line is calculated as 137,3 mm and the width as 3.36 mm for 83°

# Layout design

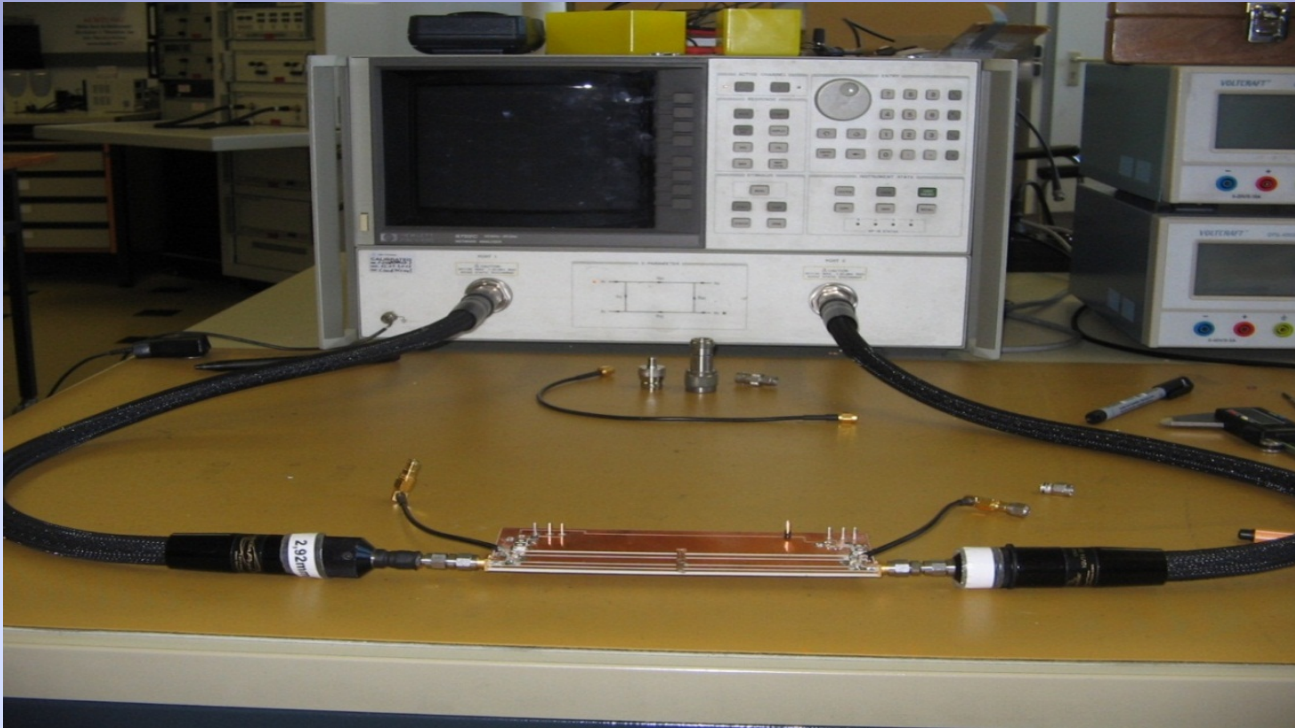




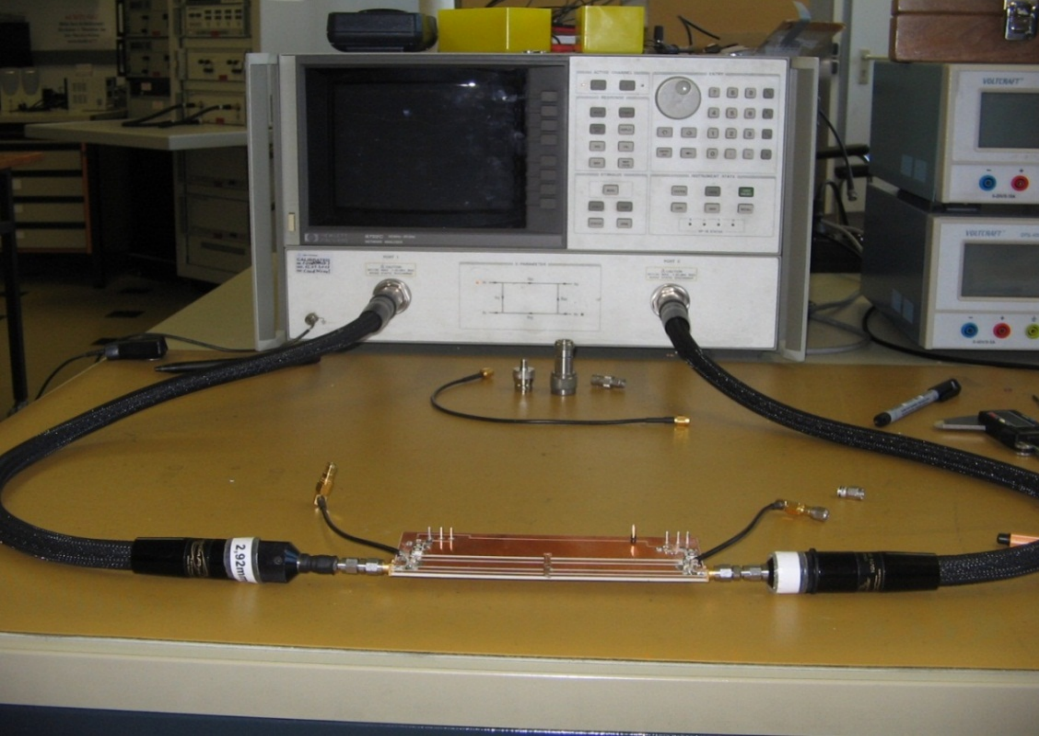
At first observation, coupling between two transmission line and parasitic effect was too high. These transmission lines are made by using micro-strip lines. As it seems that we have to change this connection.

To have less parasitic effect, microstrip line connection was cut off from sma connectors, and the long part in the middle was connected to ground. Two coaxial cables were used instead of microstrip lines. They are soldered to the back ground plane with the center conductor connected to the sma connectors through drilled holes.

# Directional Coupler Tests

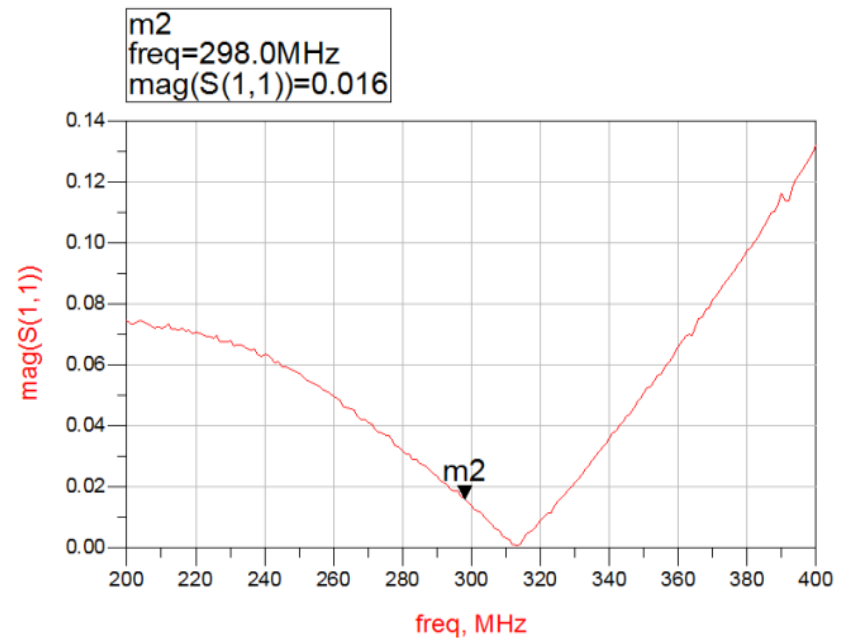
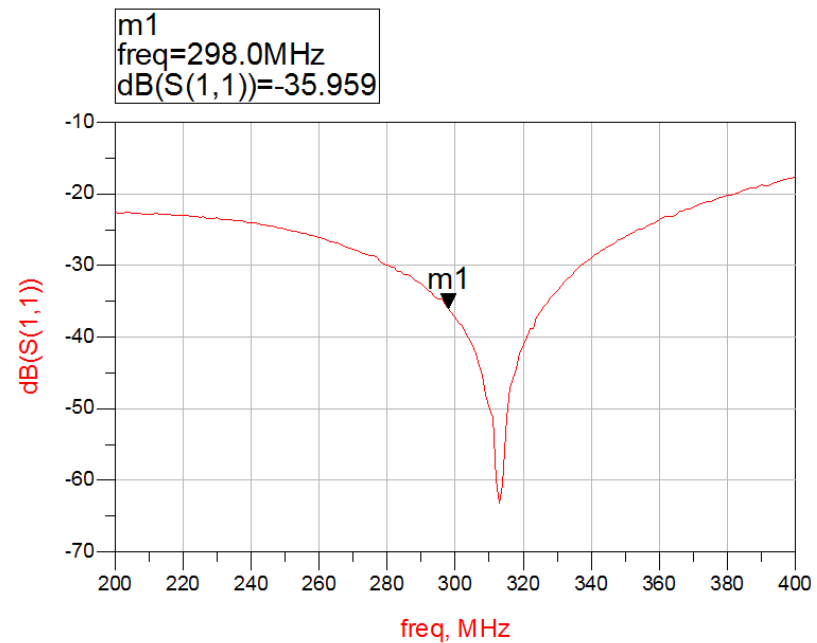


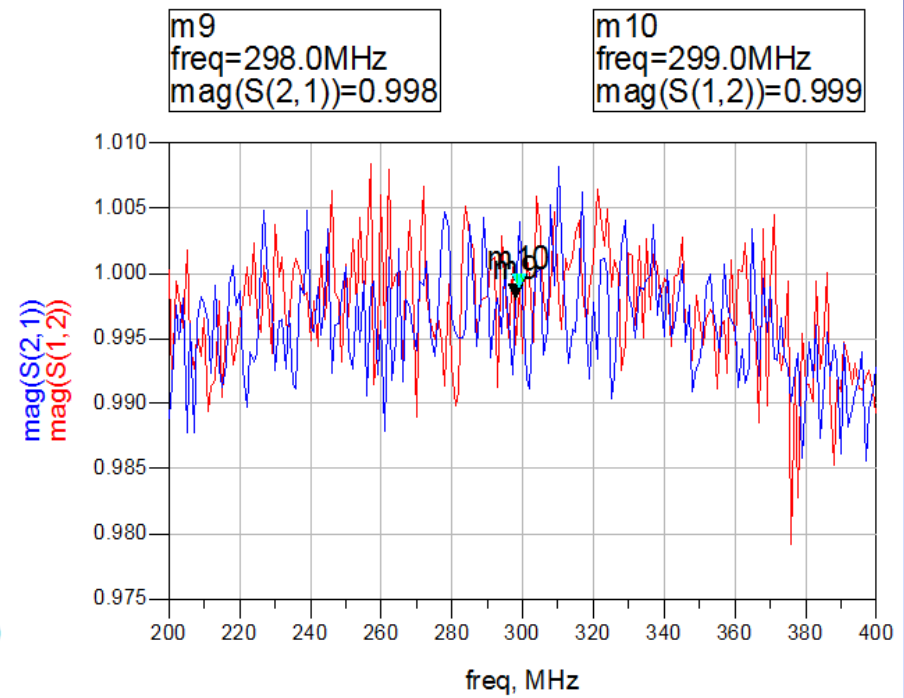
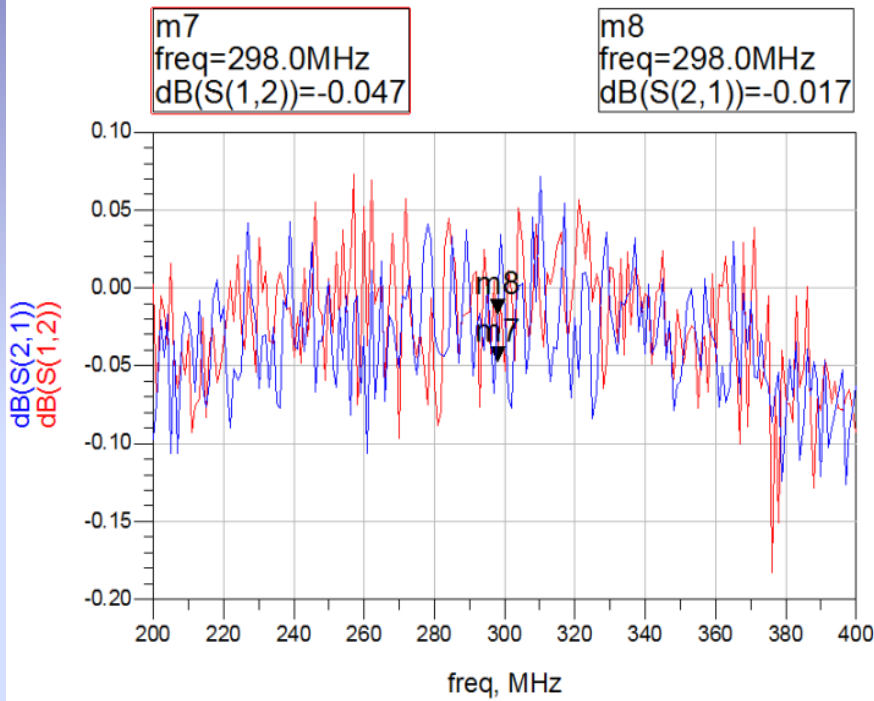
In this section scattering parameters of directional coupler are going to be tested. A network analyzer was used for testing. Two flexible coaxial cables with sma connectors were soldered to the second transmission line in order to connect it to network analyzer. It is expected to see similar results like advanced design system simulations.



## Thru Line Test

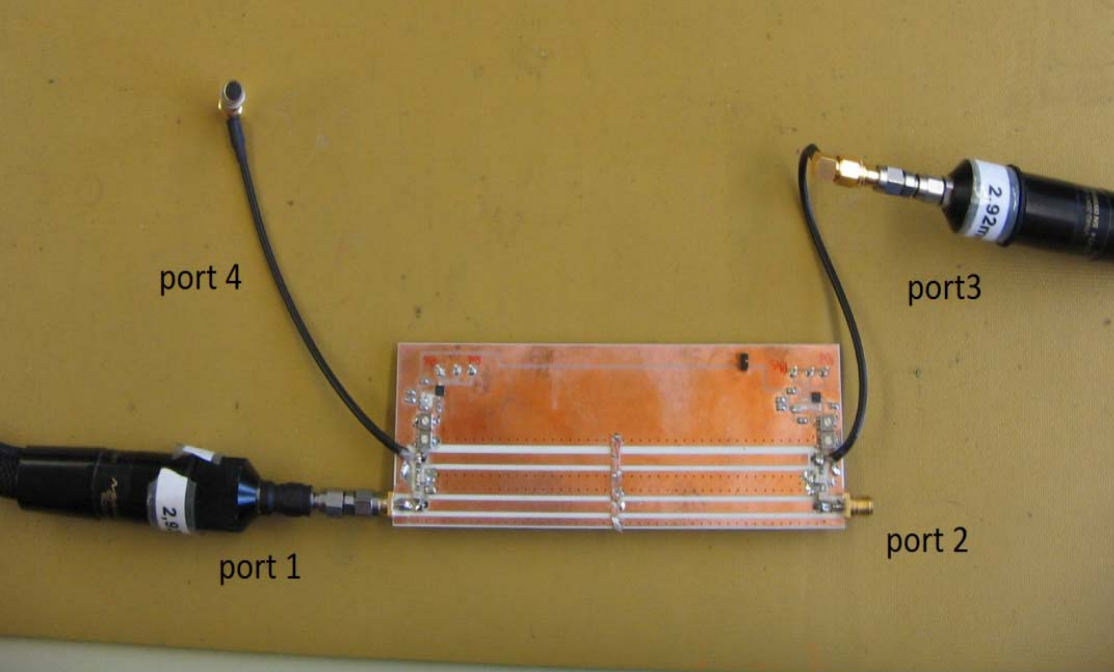
In thru line connection, port1 and port2 are connected to the network analyzer. Now we can test scattering parameters for two port network.  $S_{11}$  refers the reflection coefficient. As simulated before with Advanced Design System it should give us small values. In ADS simulation  $S_{11}$  was “-51.771 dB “and magnitude was 0.0025. In realty reflection coefficient has “-35.959 dB” and 0.016 magnitude.





$S_{12} = S_{21} = \text{transmission coefficient} .$

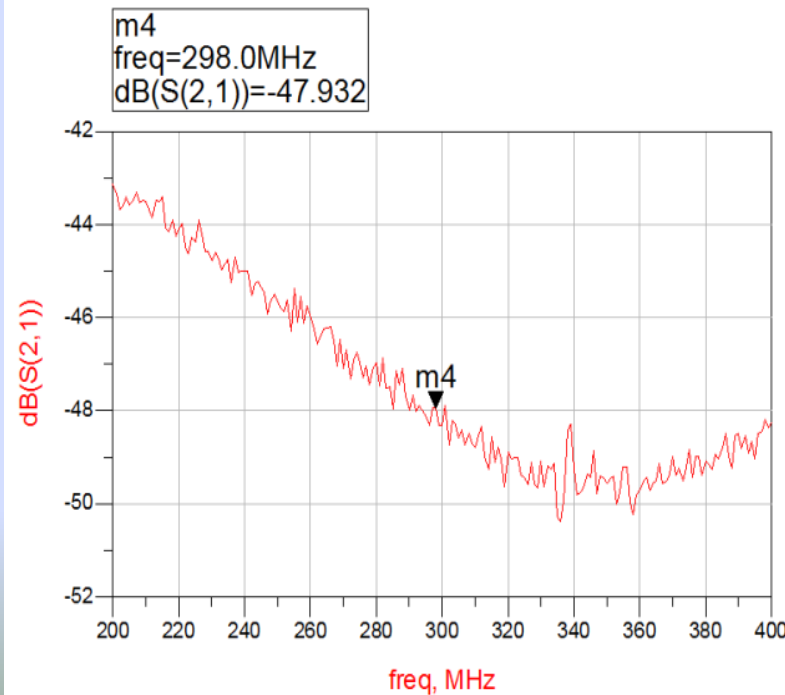
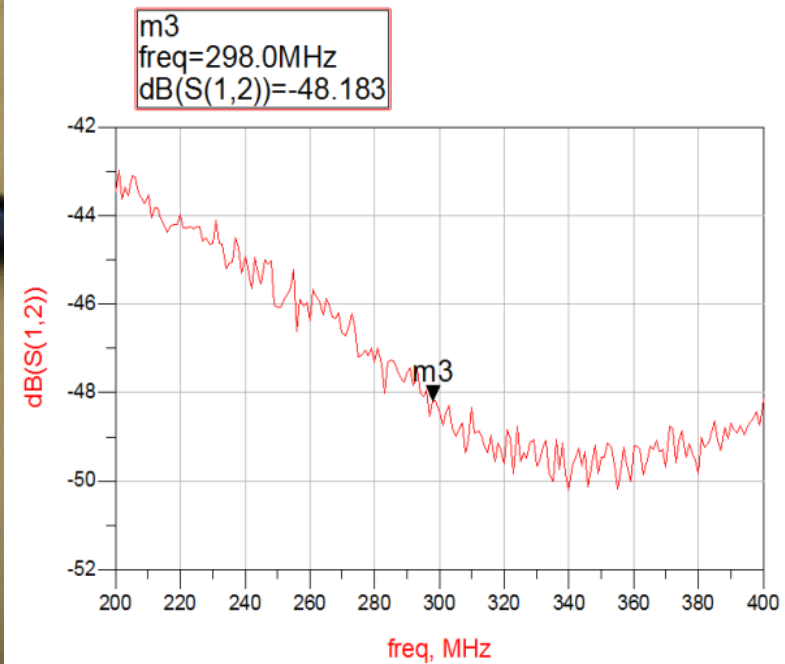
Our magnitude values are nearly 1. We can consider that all travelling wave passes through and reflection of that signal is nearly zero

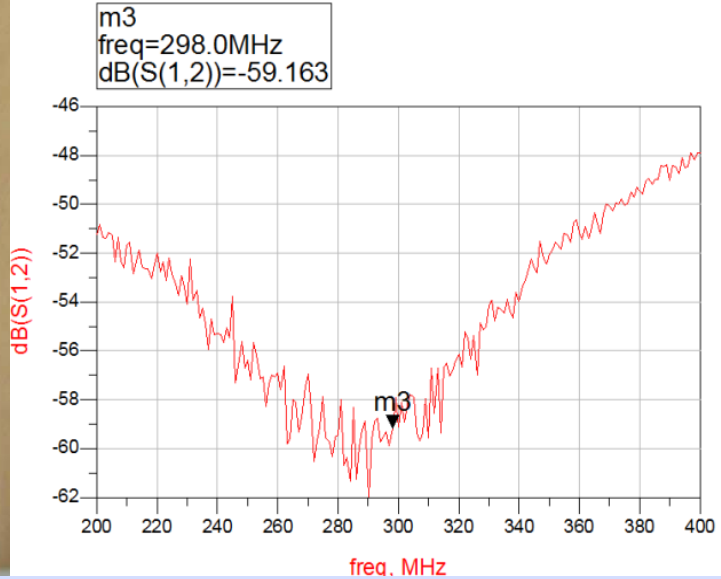
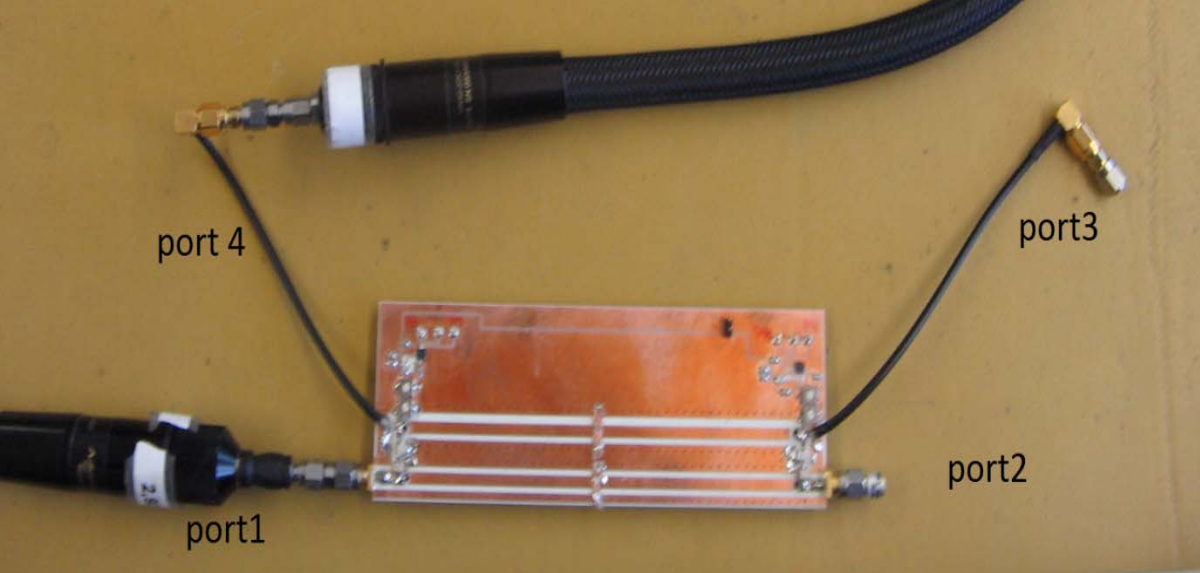


## Coupled Line Test

In this section coupling factor of directional coupler is going to be tested. Using the same network analyzer, port1 and port3 were connected. We are expecting a coupling factor around "-45 dB". All ports are terminated with  $50\Omega$ . In theory  $S_{12} = S_{21}$ .

We got coupling factor around "-47 dB" in reality. That equals 0.004 in magnitude. It means that nearly none the RF signal which enters to port1 occurs in port3.





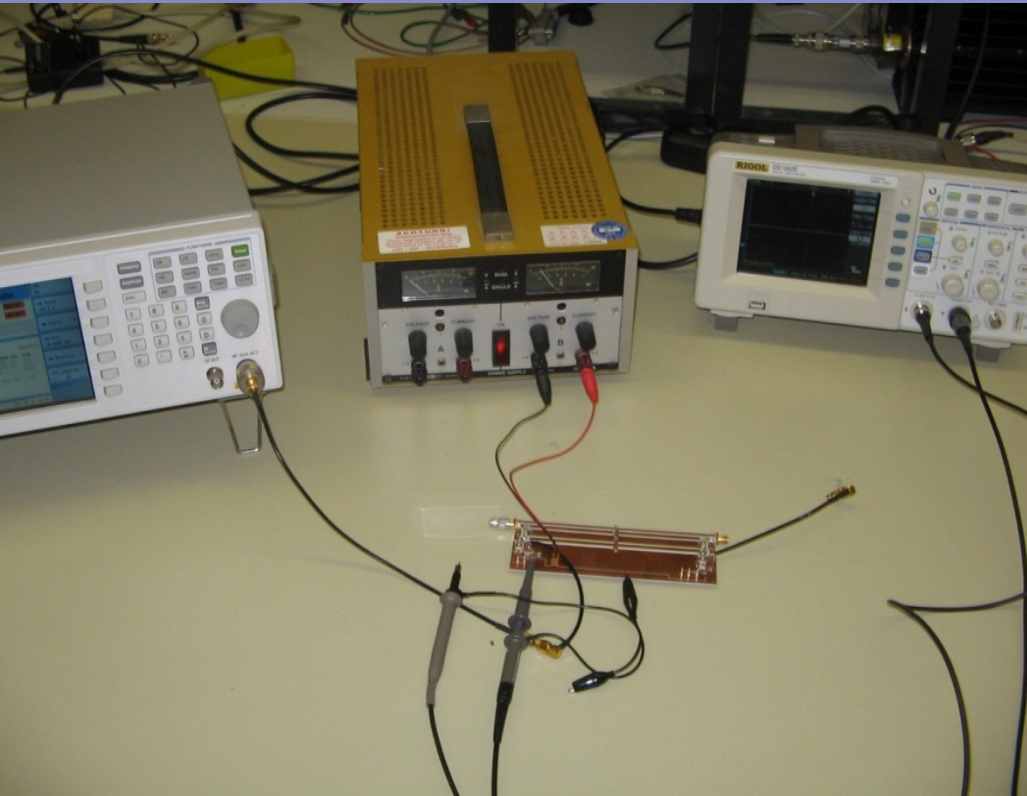
## Isolated Line Test

Using the same network analyzer, port1 and port4 were connected. We are expecting that there is no signal flowing between these two ports.

The measured isolation of “-59 dB” is a very small number and equals 0.001 in magnitude. It is smaller than  $S_{12}$  in coupling test. That condition proves us that these two transmission lines work as directional coupler. According to equation

- Isolation (dB) = Coupling (dB) + Directivity (dB)
- - 59 dB = - 48 dB + Directivity(dB)    Directivity = ” -11 dB”

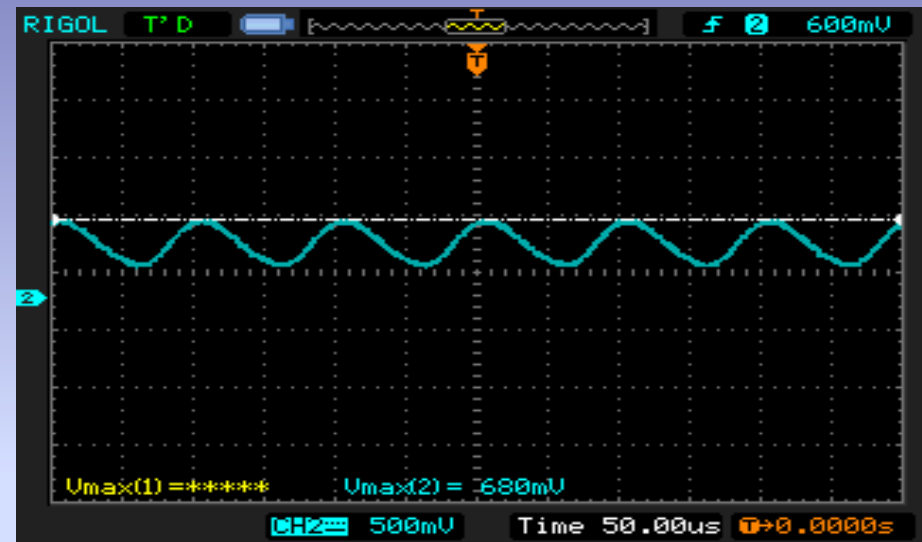
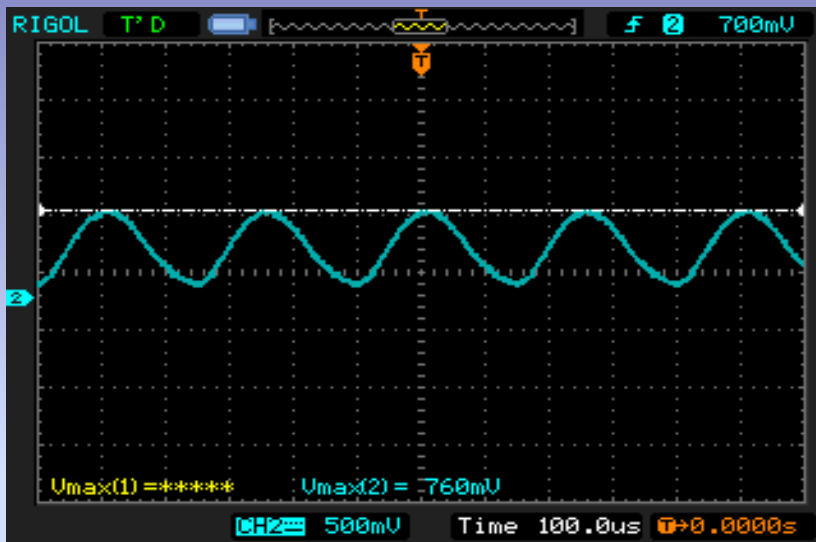




With reference to datasheet of ADL5511 envelope equals  $(V_{ENV} - V_{EREF})$ . Due to conversion gain of  $V_{ENV}$  and  $V_{RMS}$ , a new equation can be written with reference to Table 1 on page3 in datasheet of ADL5511.

$$\frac{(V_{ENV} - V_{EREF})}{1.42} = \frac{VRMS}{1.92}$$

In this section the circuit connected to the second transmission line is going to be tested. For input signal sma connections should be changed. Sma connectors with cable were connected to the transmission line in directional coupler tests. Now they were soldered to the beginning of trim resistors and the connection to the directional coupler ports were cut by a knife for circuit tests. Our test system consists of a signal generator, power supply and an oscilloscope. For attenuation, trim resistors values should be set up. These values were discussed before. The circuit include three different outputs;  $V_{EREF}$ ,  $V_{ENV}$ ,  $V_{RMS}$ . The order is the same on circuit board for the left and right circuits.

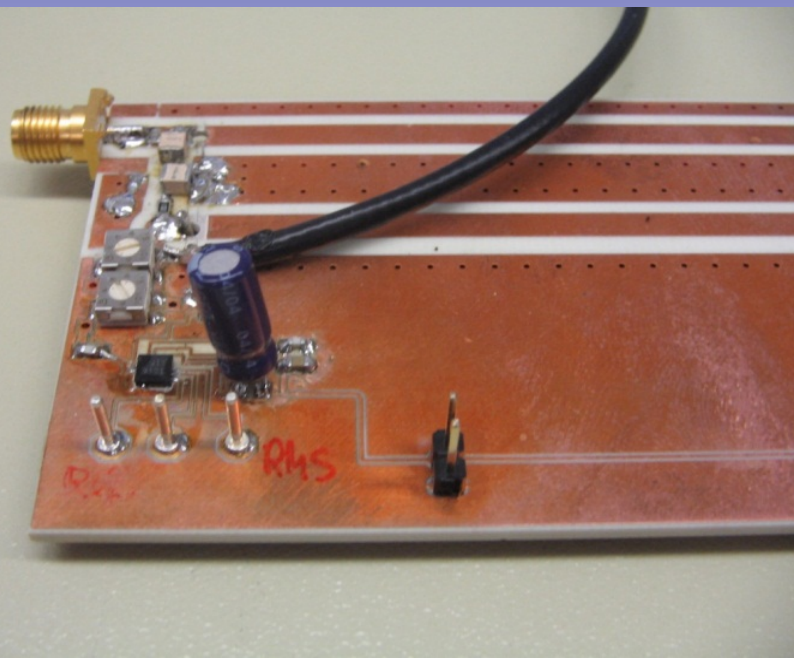


$V_{RMS}$  output for 4 kHz and 10 kHz a.m. ,300MHz ,amplitude 10dBm

$V_{RMS}$  can be defined as;

- $$V_{RMS} = A \times \sqrt{\frac{\int_{T1}^{T2} Vin^2 x dt}{T2-T1}}$$

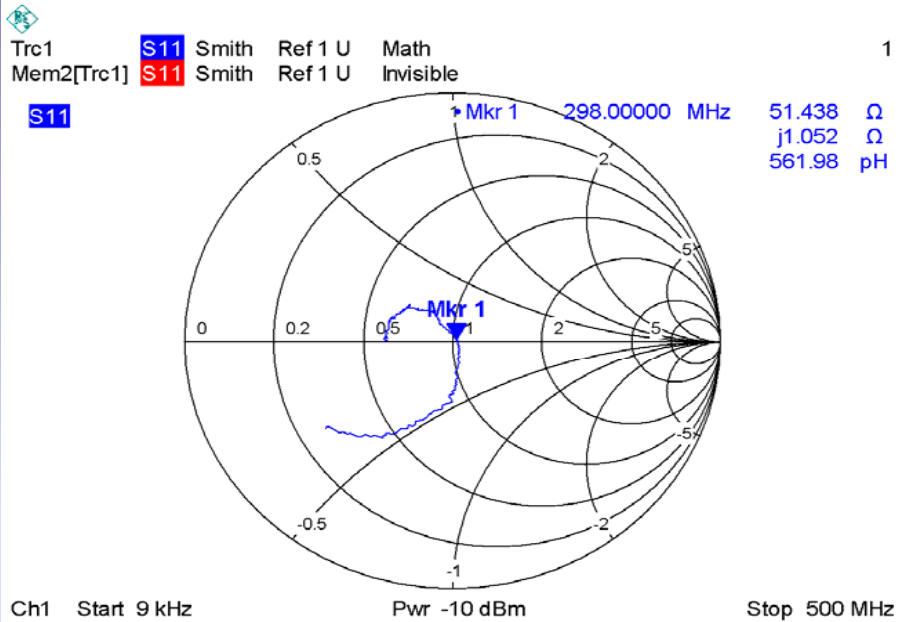
A is a scale of resistor ratio on chip. Due to equation ,  $V_{RMS}$  is square root of integrated ( $V_{in}^2$ ) from T1 to T2. However, the circuit uses a low –pass filter for the integration. In our measurements an unexpected zigzag is seen at  $V_{RMS}$  output. This is an effect of the approximate character of the integration by a low-pass filter. It affects the quality of measurements.



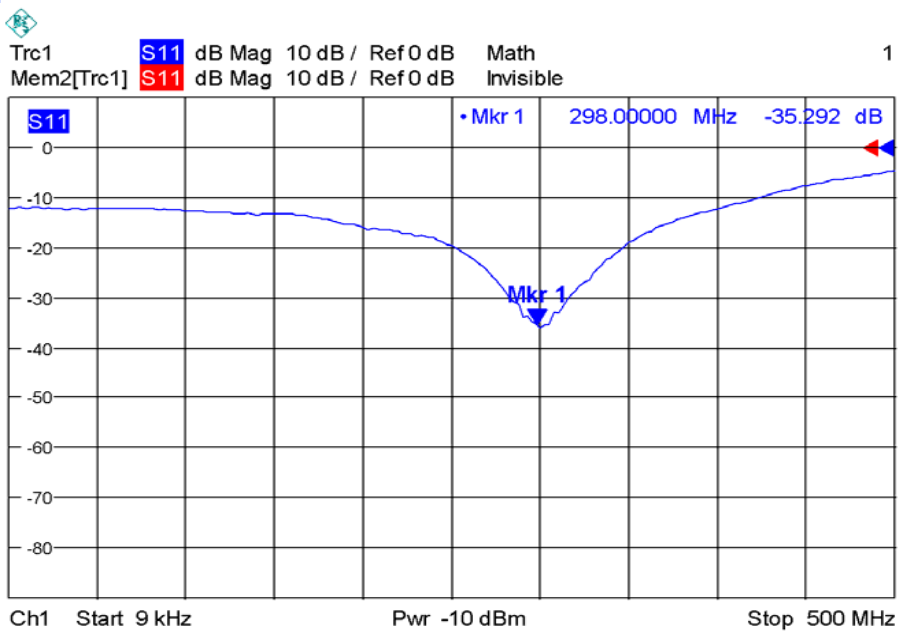
$$f_{\text{filter4}} = \frac{1}{(2\pi \times C_{\text{FLT4}} \times 400\Omega)}$$

$$\frac{1}{(2\pi \times 33\mu\text{F} \times 400\Omega)} \cong 12 \text{ Hz}$$

$V_{\text{RMS}}$  output voltage should be seen as a straight line. As it seems, at 4 kHz frequency output voltage has a peak to peak value and starts from zero point, but at 10 kHz amplitude decreases and output value starts from a certain value. It means that if frequency increases, time difference between T1 and T2 decreases and according to  $V_{\text{RMS}}$  equation, we get more stable outputs. The low pass filter4 should be reduced to block zigzags. If we reduce filter4 around hundred times smaller, it expected to see more efficient signal outputs.

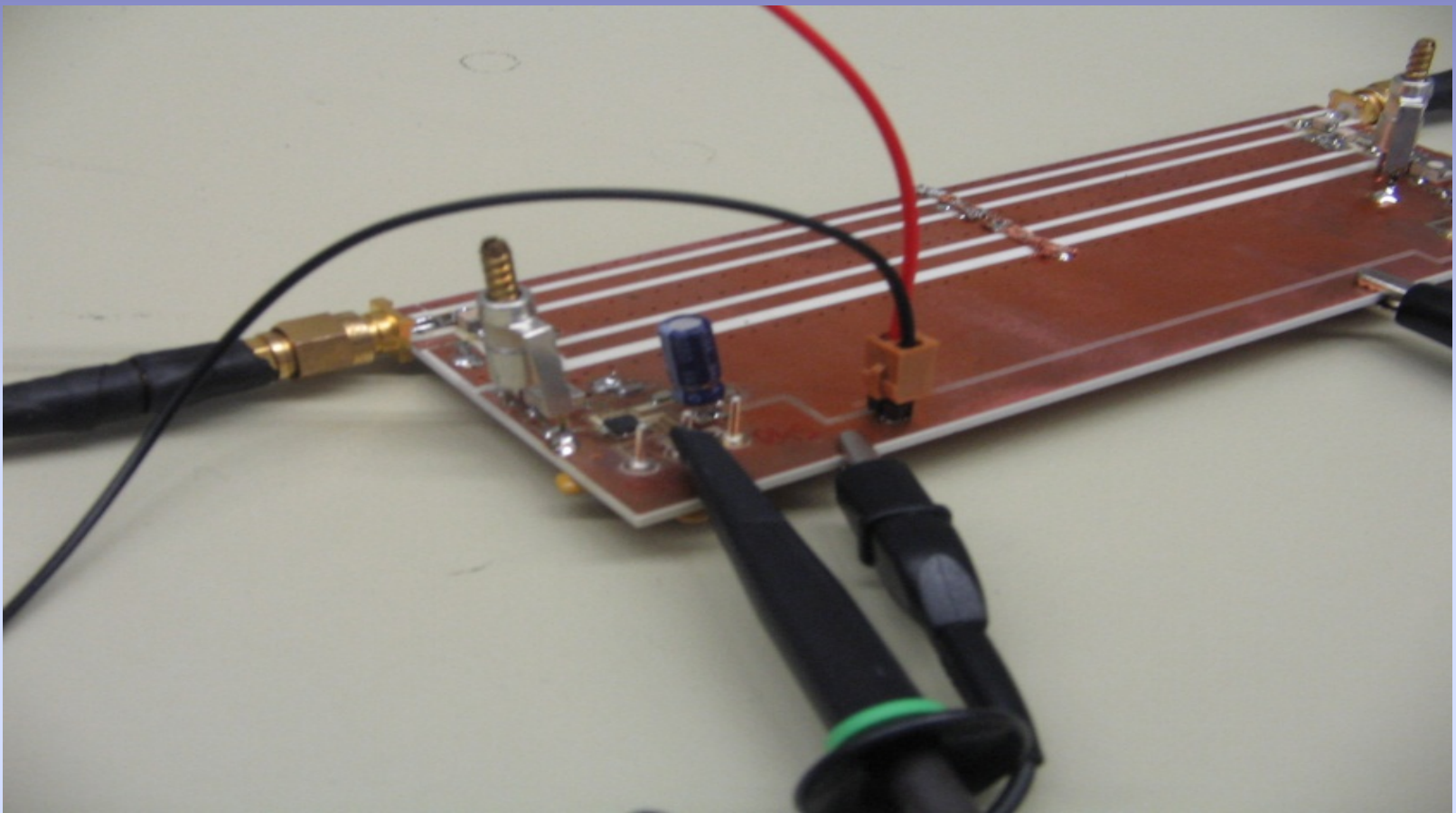


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Date: 9.FEB.2012 13:14:07

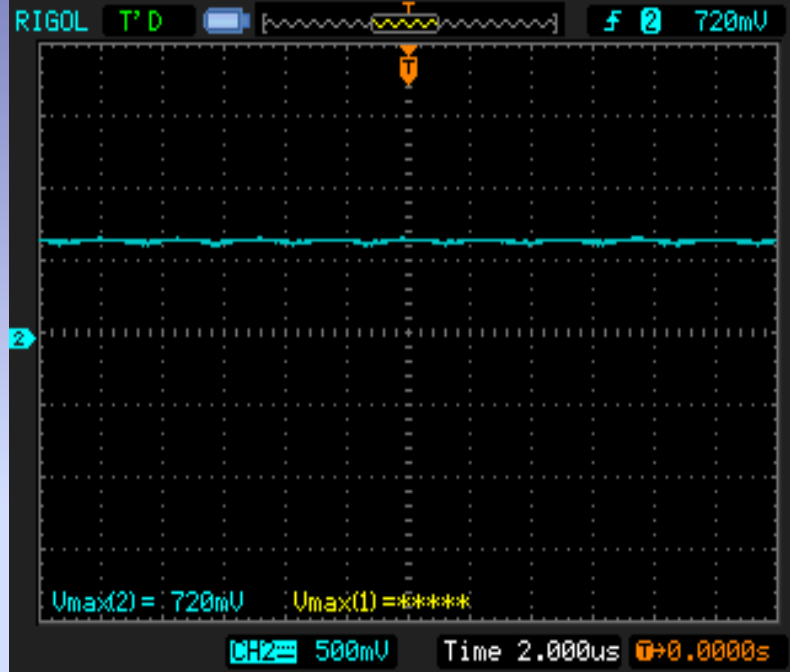
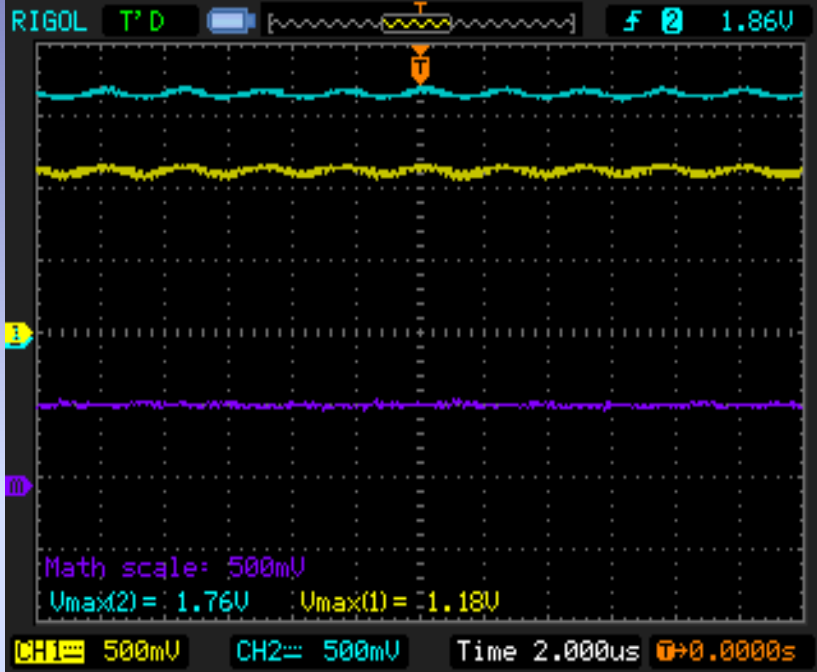


PA  
Date: 9.FEB.2012 13:13:48

A perfect match for 50 ohm is needed for the input to the detector circuit, in order that the directional coupler is correctly terminated. Otherwise directivity changes and suffers for measurements. Network analyzer was used to check 50 ohm match and we got  $60 + j15$  ohm. This value should be reduced. To get 50 ohm match an adjustable capacitance was connected parallel to two trim resistors.



If we compare the result that was done before with for  $S_{11}$ , It can be seen that we have a perfect match in reality. The new values for trim resistors are  $R_a = 7$  ohm,  $R_b = 24$  ohm (left side of the circuit board) and  $R_a = 3.5$  ohm,  $R_b = 23.2$  ohm (right side of the circuit board). The added capacitance is 6 pF.

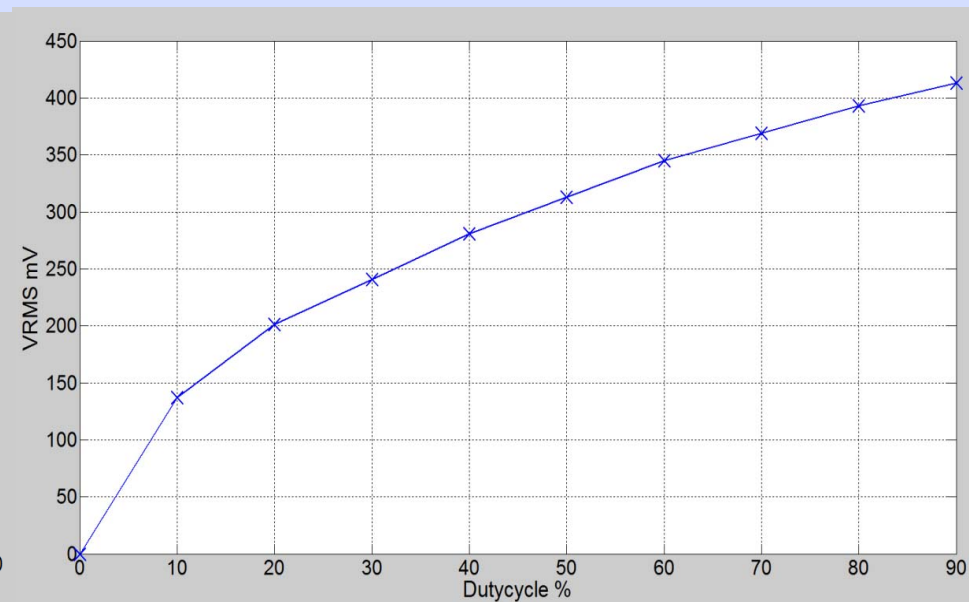
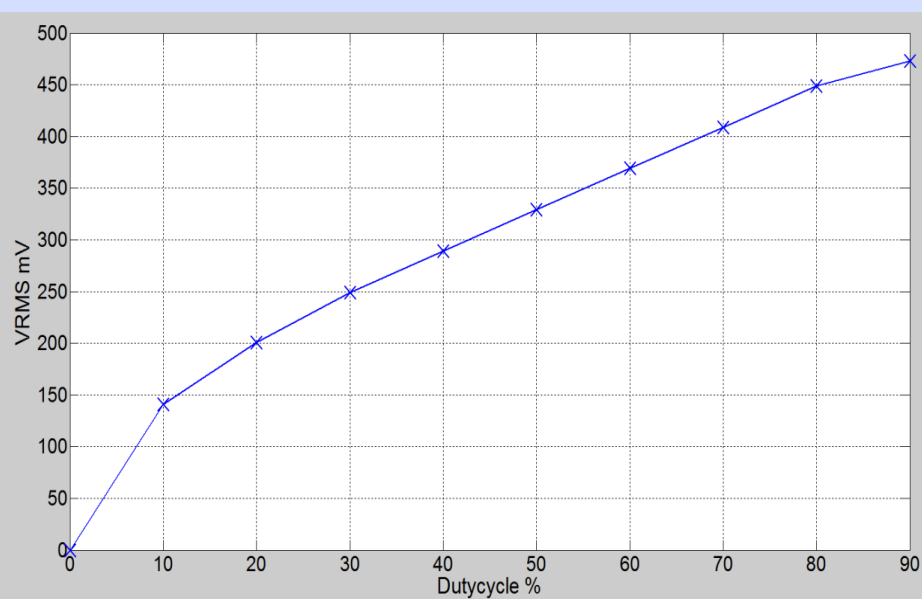


outputs  $V_{ENV}$  and  $V_{RMS}$  measurements for 20 Hz at the right detector circuit

- For 20 Hz  $V_{ENV} = 1.76$  V,  $V_{REF} = 1.18$  V, Envelope Voltage =  $(1.76 - 1.18) = 580$  mV

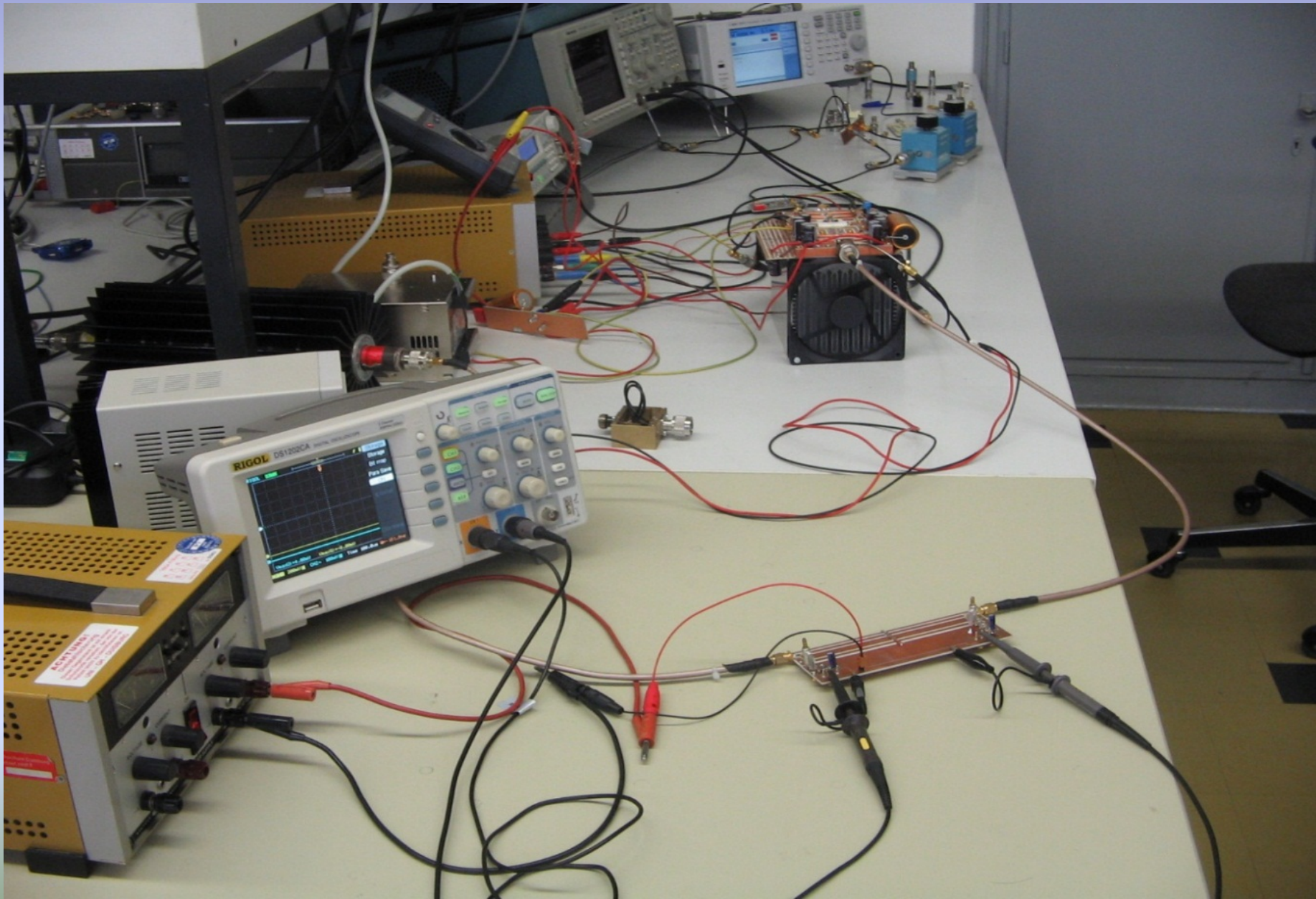
- $$\frac{580\text{mV}}{1.42} \approx \frac{720\text{mV}}{1.92}$$

Duty cycle %	Right side $V_{RMS}$	Left side $V_{RMS}$
10	141 mV	137 mV
20	201 mV	201 mV
30	249 mV	241 mV
40	289 mV	281 mV
50	329 mV	313 mV
60	369 mV	345 mV
70	409 mV	369 mV
80	449 mV	393 mV
90	473 mV	413 mV



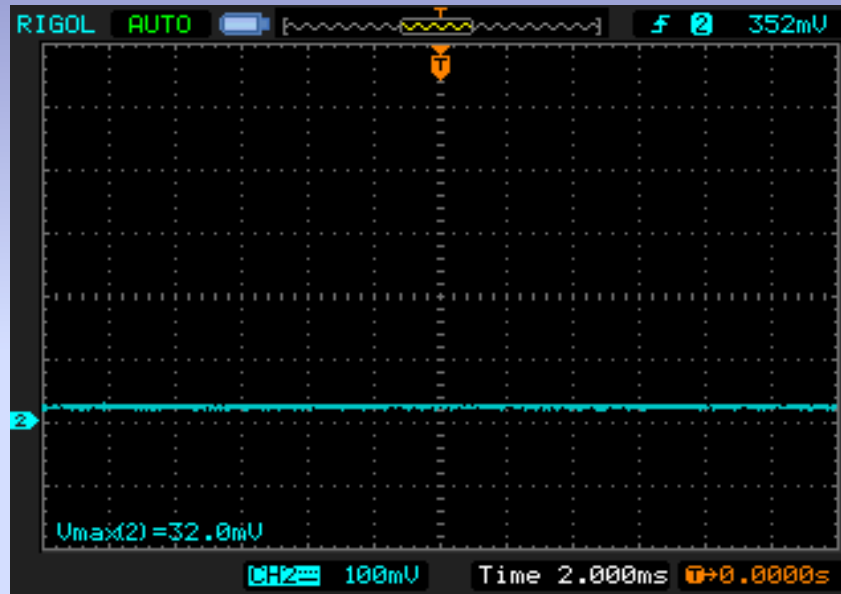
For frequency 300 MHz, 10 dBm amplitude, 1 kHz pulse frequency without 23 mV offset voltage

# Directional coupler connected to 7-Tesla Magnetic Resonance Imaging (MRI) System





# Before measuring reflection coefficient



- $V_{ENV}$  voltages at CH1=280 mV and CH2 =90 mV at 400 Hz pulse modulation for 10% duty cycle. If outputs of detectors have any offset voltages, this value should be subtracted from all values.  $V_{ENV}$  does not depend on duty cycle.
- Reflection coefficient can be calculated from ratio of voltages. But before that calculation residual dc voltages at outputs should be measured. . Figure shows residual dc voltage at CH1 for the  $V_{ENV}$  measurements. Residual dc voltages for CH1=32 mV CH2= 26 mV.

Duty cycle %	CH1 without offset voltage	CH2 without offset voltage
5	114-32= 82mV	48-26 = 22mV
10	154-32= 122mV	58-26 = 32mV
20	204-32 = 172mV	72-26 = 46mV

$$\Gamma = \frac{48mV - 26mV}{114mV - 32mV} = \frac{22mV}{82mV} = 0.26$$

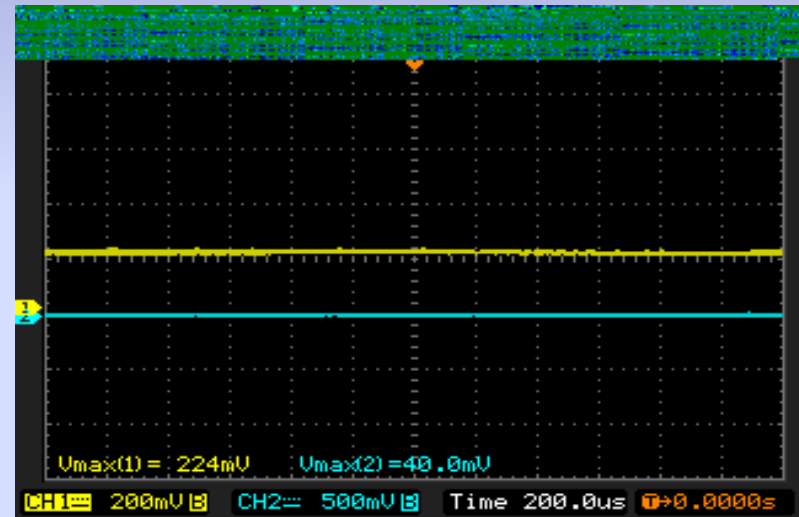
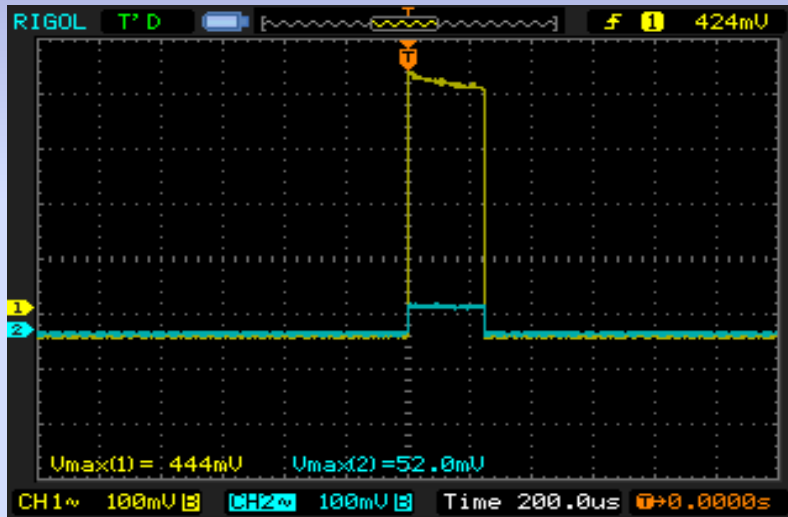
$$\Gamma = \frac{58mV - 26mV}{154mV - 32mV} = \frac{32mV}{122mV} = 0.26$$

$$\Gamma = \frac{72mV - 26mV}{204mV - 32mV} = \frac{46mV}{172mV} = 0.26$$



3.3 $\mu$ F capacitances were connected to block residual voltages at  $V_{\text{RMS}}$  outputs of detectors and to block residual RF voltages at all outputs. Ceramic capacitors of 10 nF were shunt connected to ground

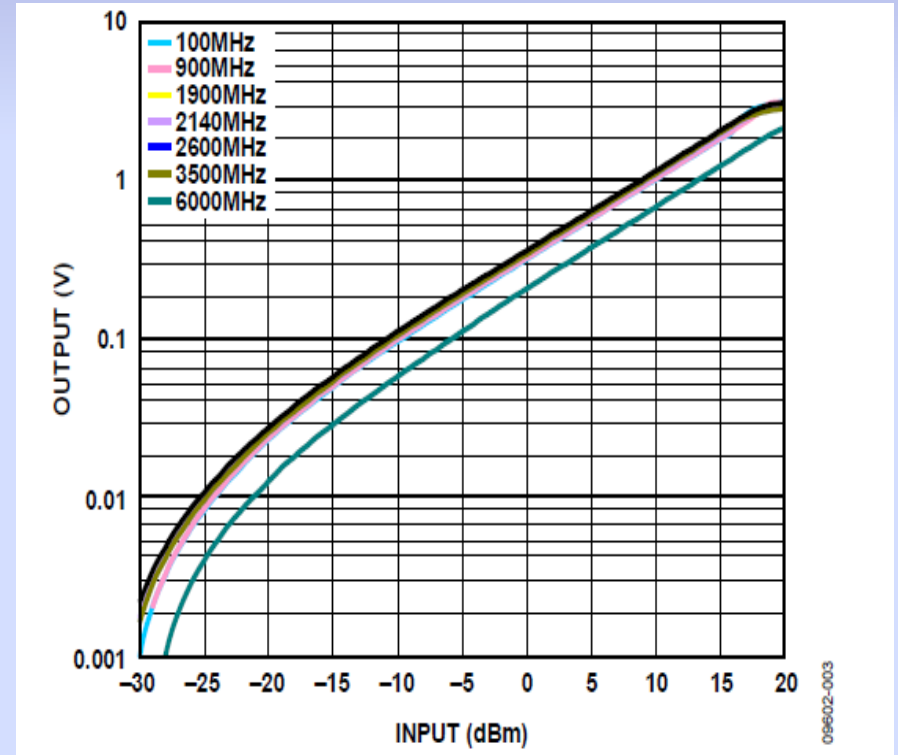
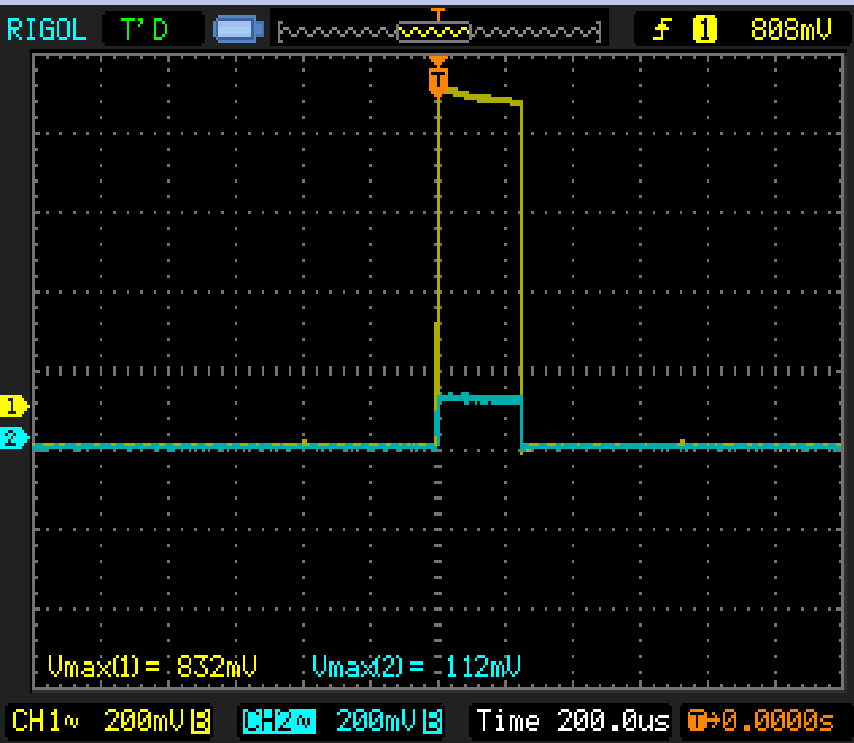
Now we can check again reflection coefficients for 250 W, 300 MHz, 400 Hz amplitude modulation with %10 duty cycle.



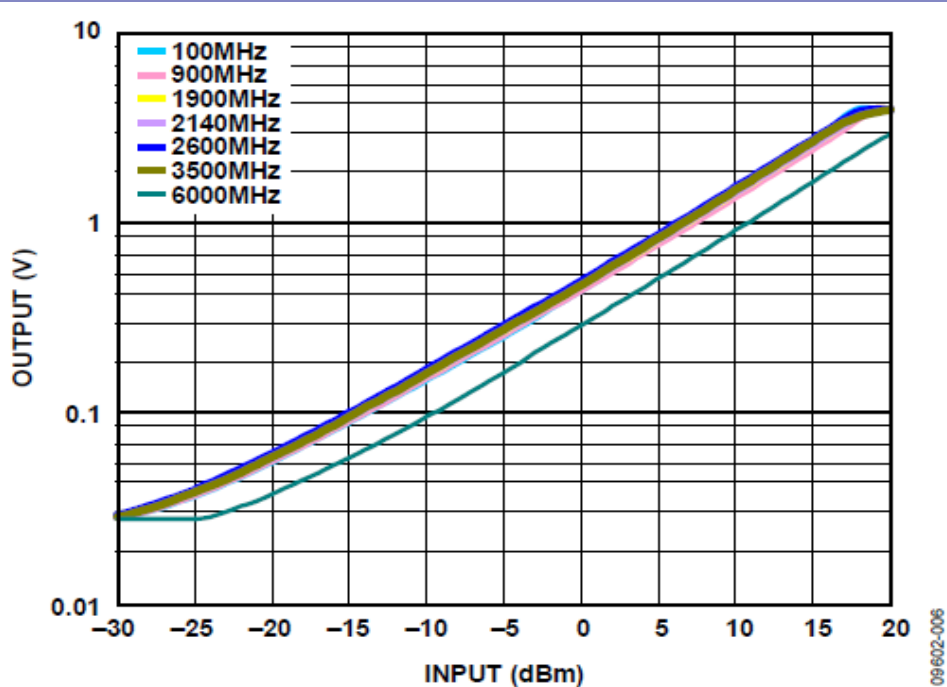
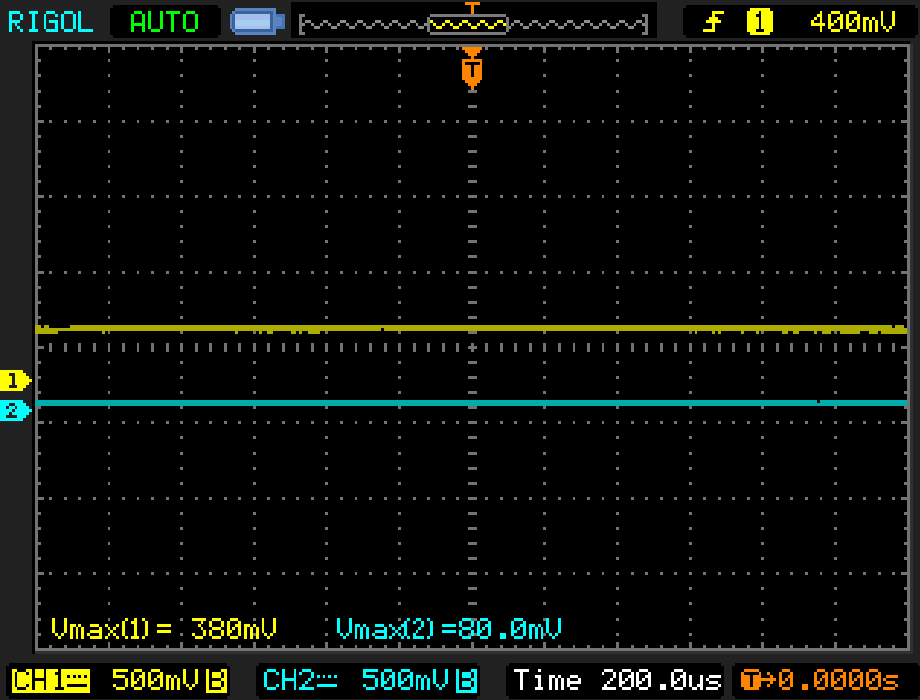
$V_{ENV}$  and  $V_{RMS}$  output voltages without residual voltage

- $\Gamma_{VRMS} = \frac{44mV}{224mV} = 0.196$
- $\Gamma_{VENV} = \frac{52mV}{444mV} = 0.117$

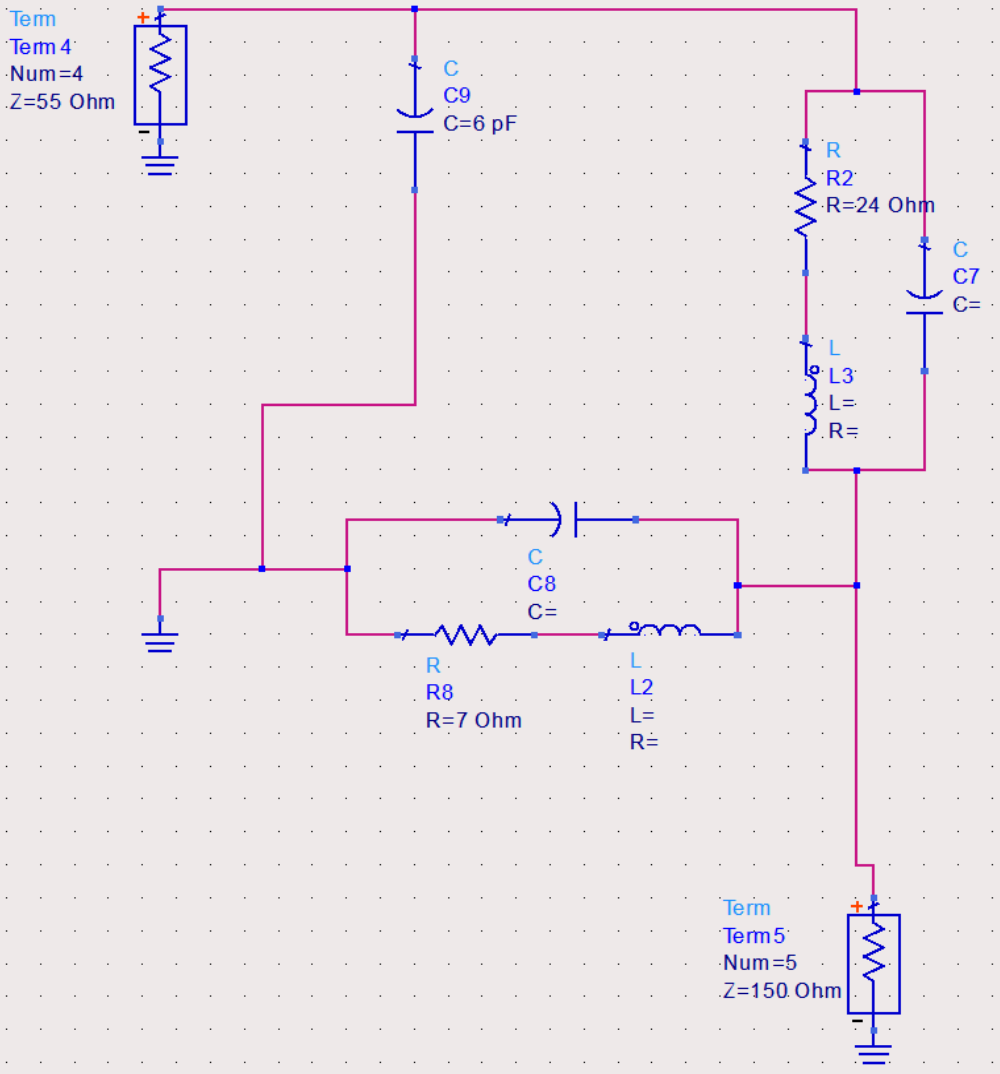
Directional coupler design can be tested now for full power; 1kW, 300MHz, 400 Hz amplitude modulation with %10 duty cycle



832 mV output voltage for  $V_{ENV}$  gives out around 8 dBm input level according to graphic

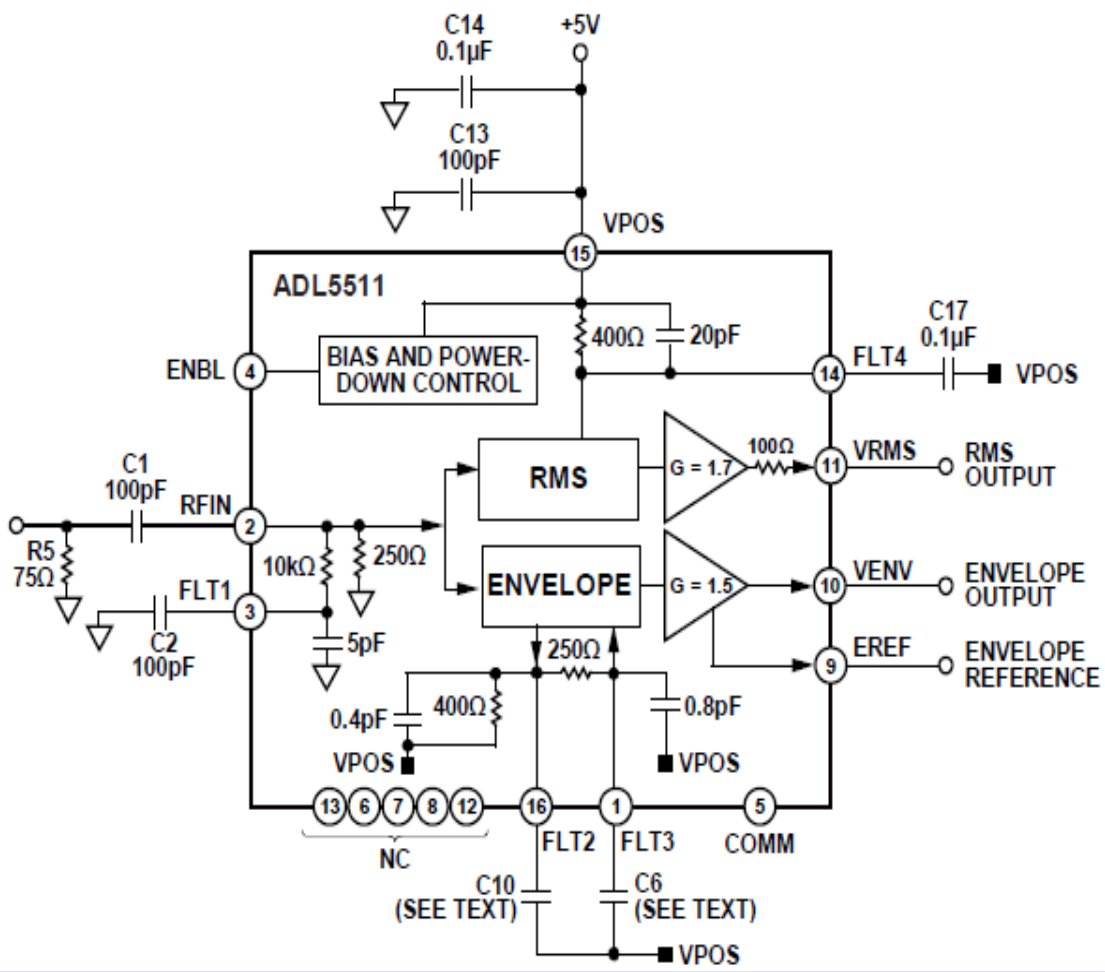


$V_{RMS}$  depends on duty cycle. 380 mV output with 10% duty cycle gives out around -2 dBm input level according to Figure 82. The difference between 10% duty cycle and 100% duty cycle equals 10 dB. If this value is added to -2 dBm, input level increases to 8 dBm for  $V_{RMS}$ . As it is expected, input levels are same for  $V_{ENV}$  and  $V_{RMS}$  output.



The power amplifier employs maximum 1 kW output power. This power equals 60 dBm input power for our directional coupler design. If coupling factor  $S_{13}$  “-45 dB” is subtracted from 60 dBm input, 15 dBm power level occurs at the input of the circuit between transmission line and detector ADL5511. That circuit consists of two series trim resistors and parallel a trim capacitor. A series inductance and a parallel capacitance to the resistors are going to be used to simulate parasitic effect. By adjusting trim resistors and trim capacitance the value of impedance was reduced from “60+j15” ohm to “51.43 + j1.05” ohm to get a perfect match. We have the first and the second case for resistor value to find out the parasitic component values. But if trim resistor is adjusted, the values of series inductance and parallel capacitance change according to resistor value

These values are not stable. Because of that it is impossible to calculate parasitic inductance and parasitic capacitance by using two impedance values.



If we assume that capacitance and inductance values are too small, power level at the detector input can be approximately calculated. But a comparison method cannot be followed, because completely different circuits were used for 50 ohm matching in our design and before preparing graphics of output voltages vs. frequency in ADL5511 datasheet.

However, it is sure that the input power to the detector circuits is below the limit for safe operation based on the generated output values.